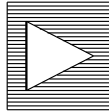
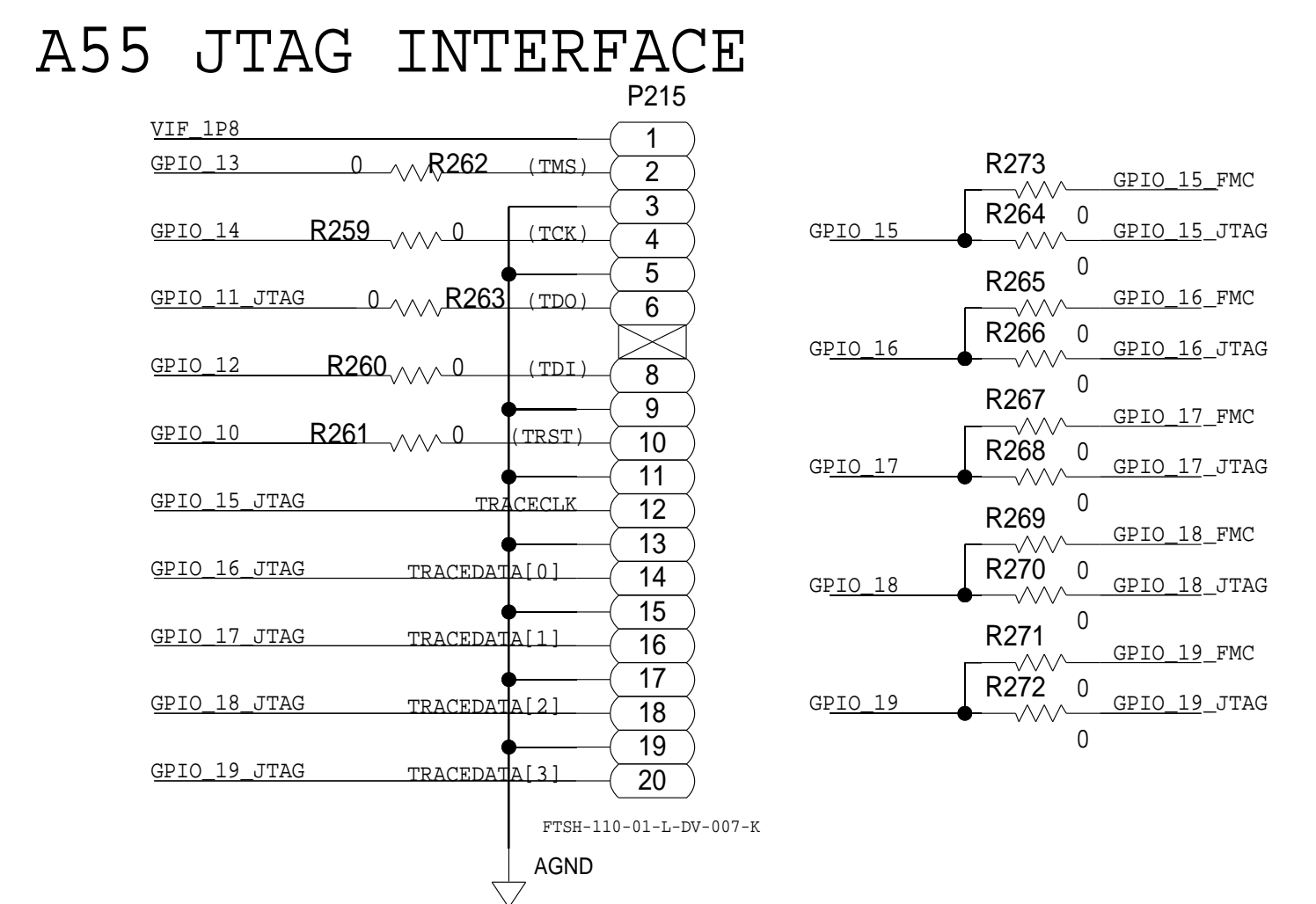
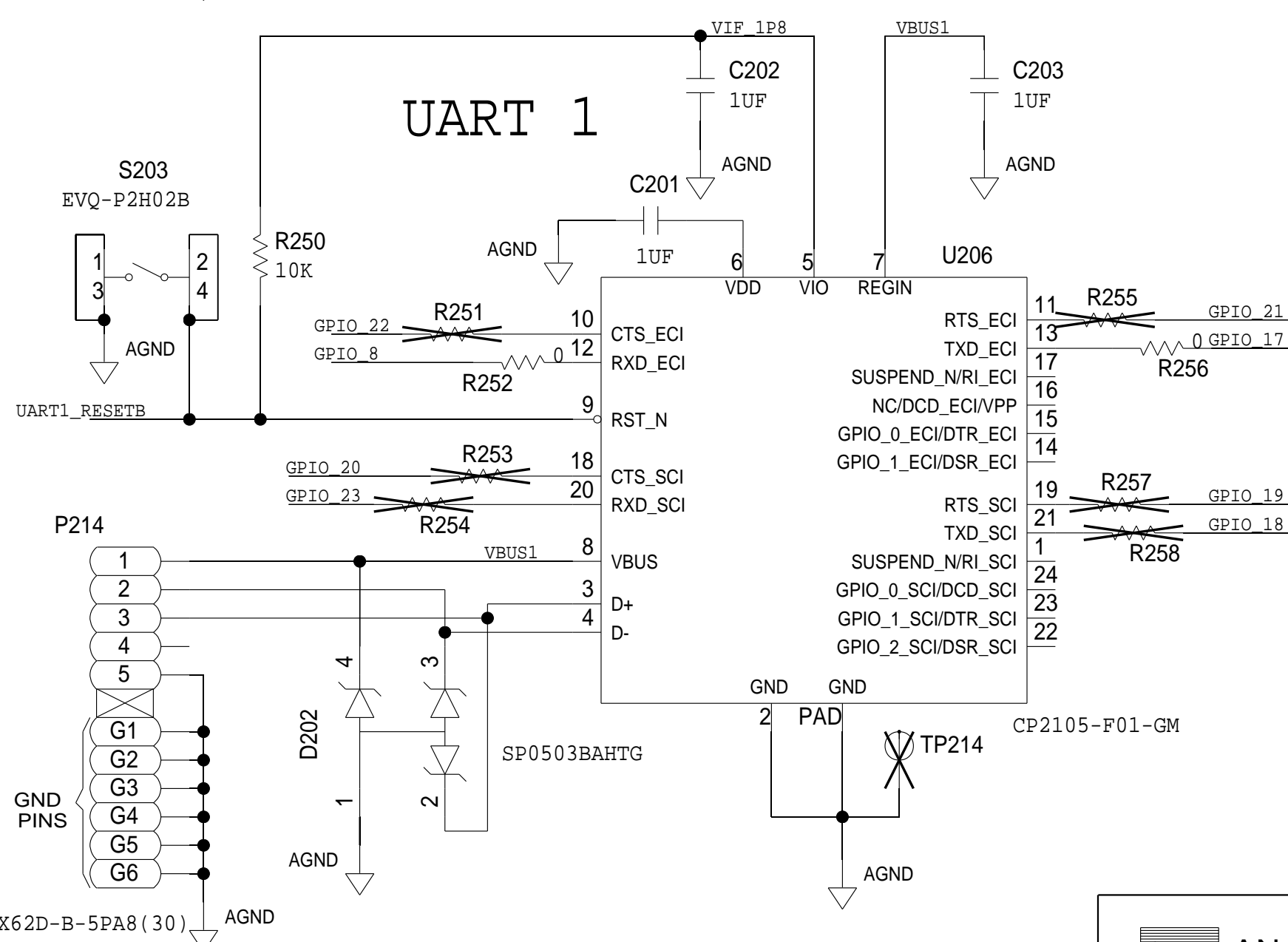
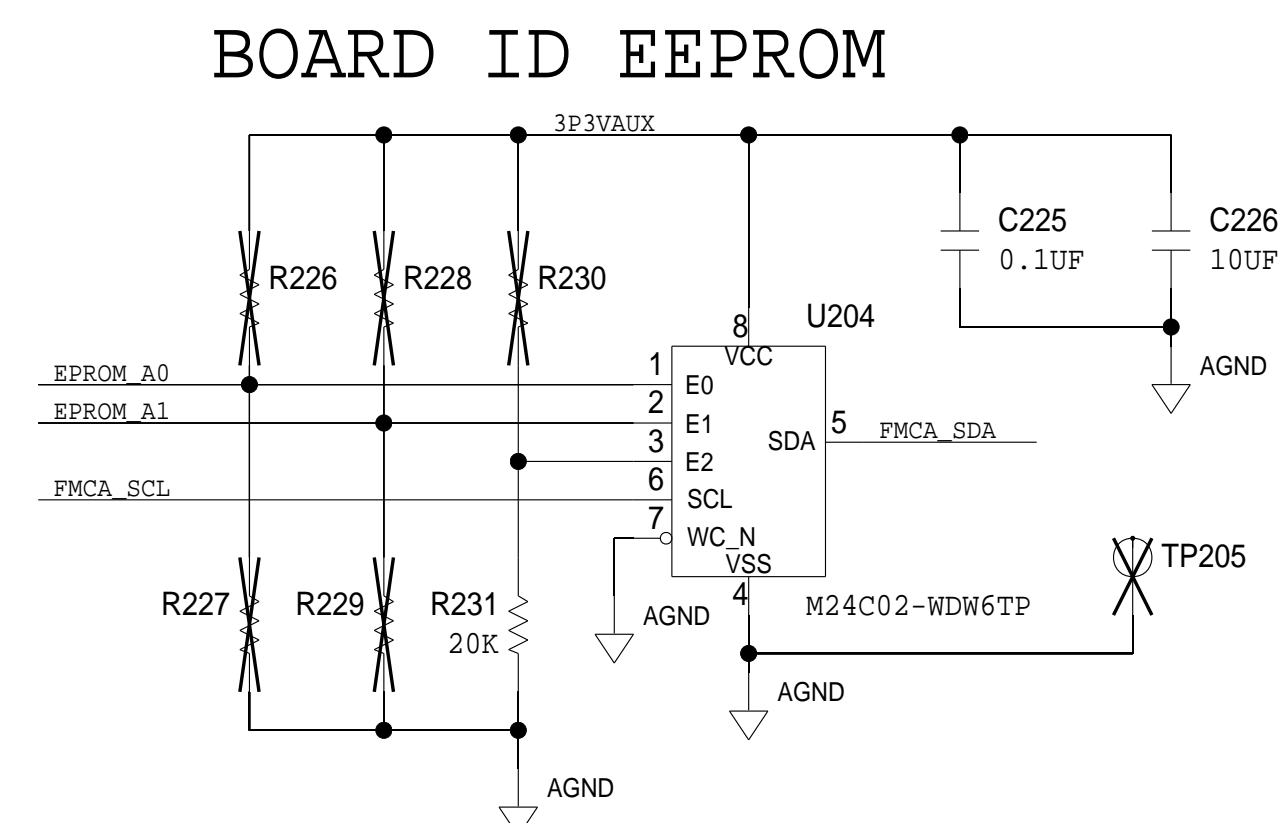
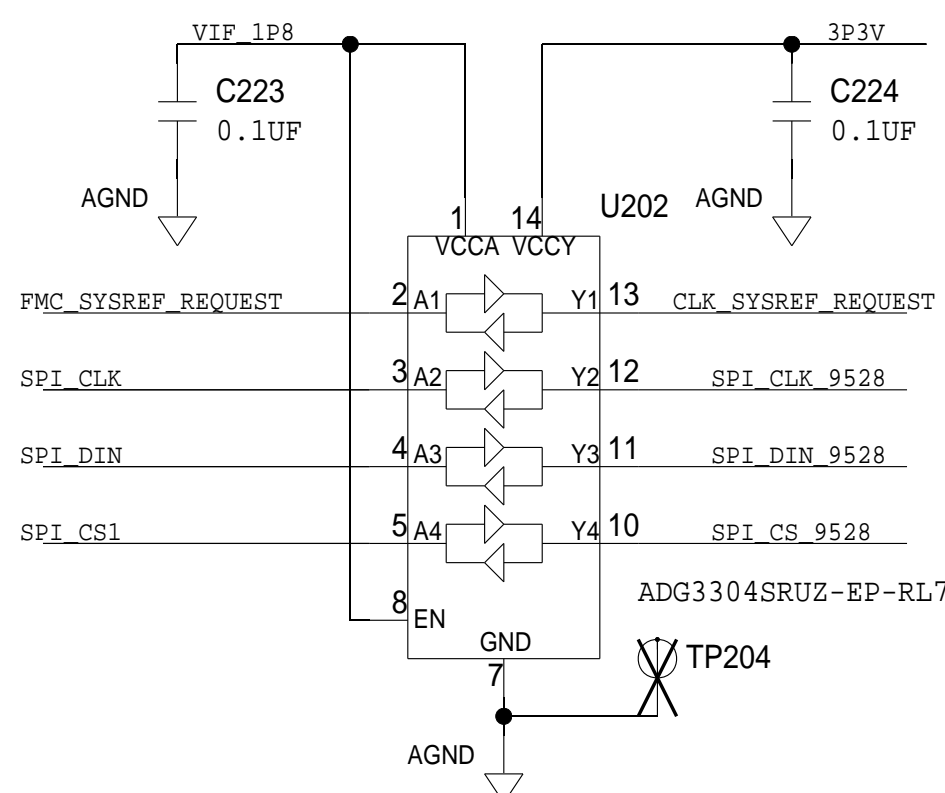
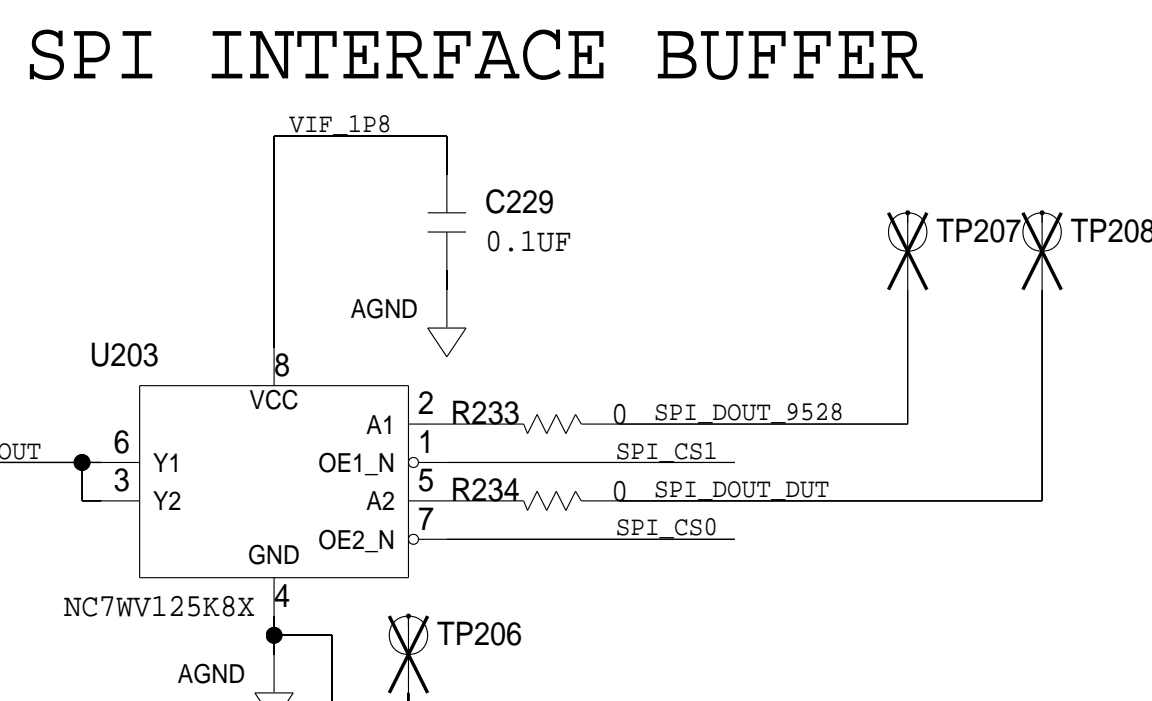
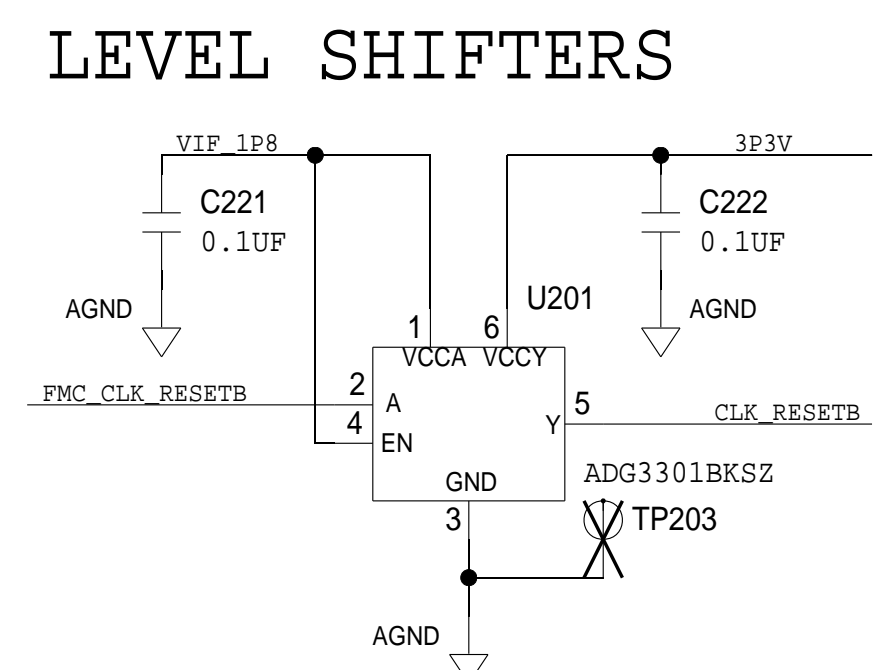
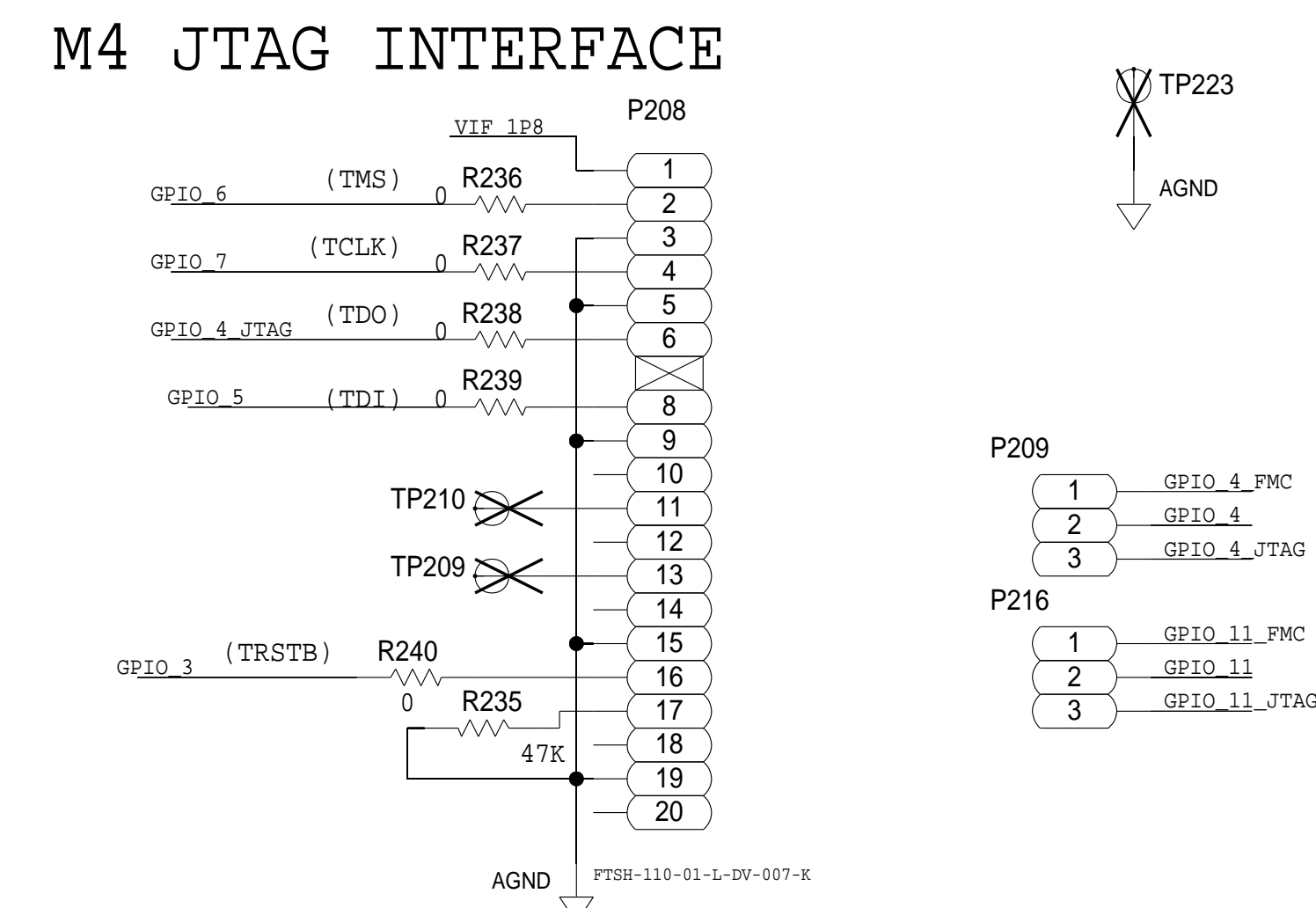
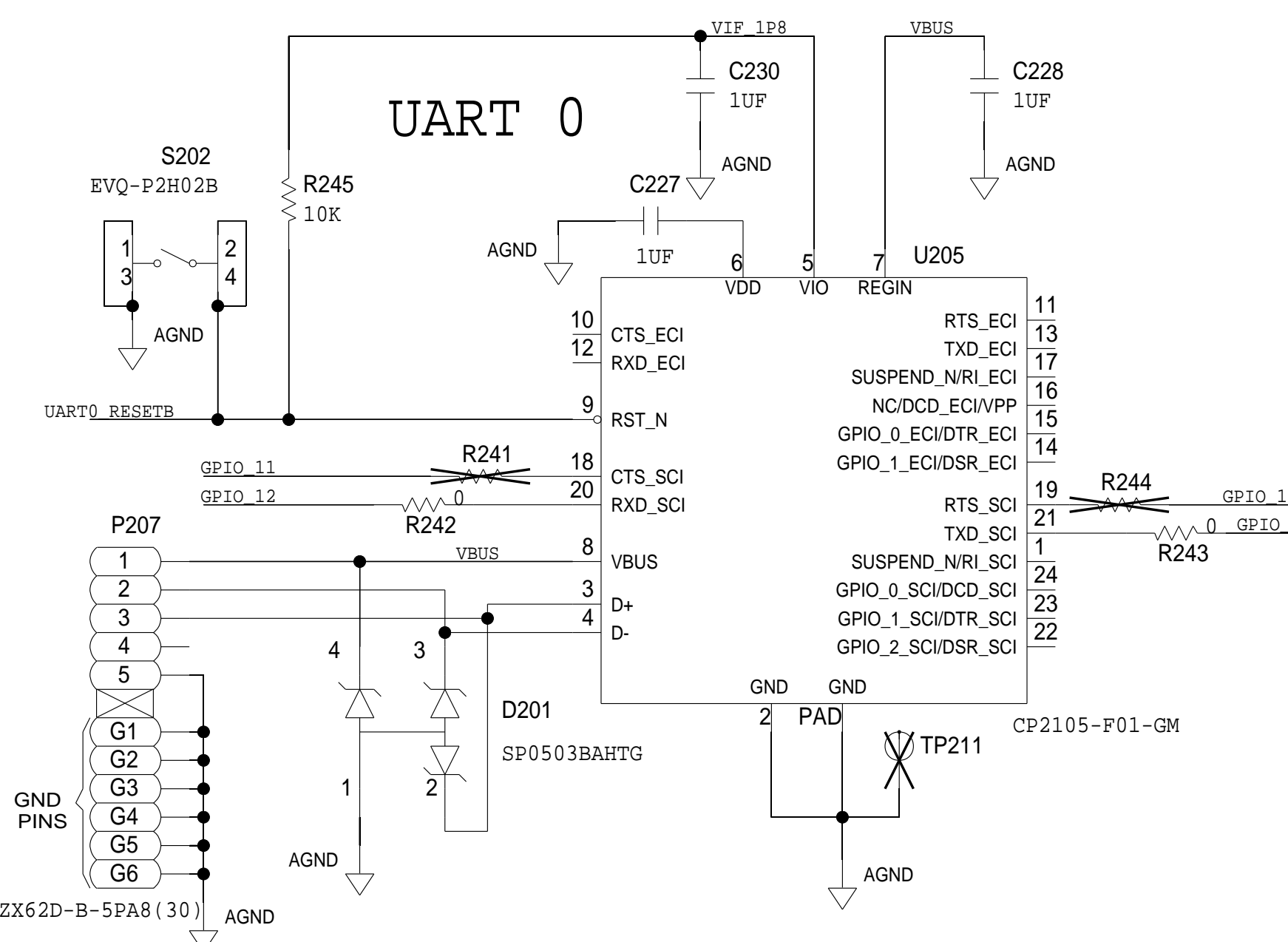
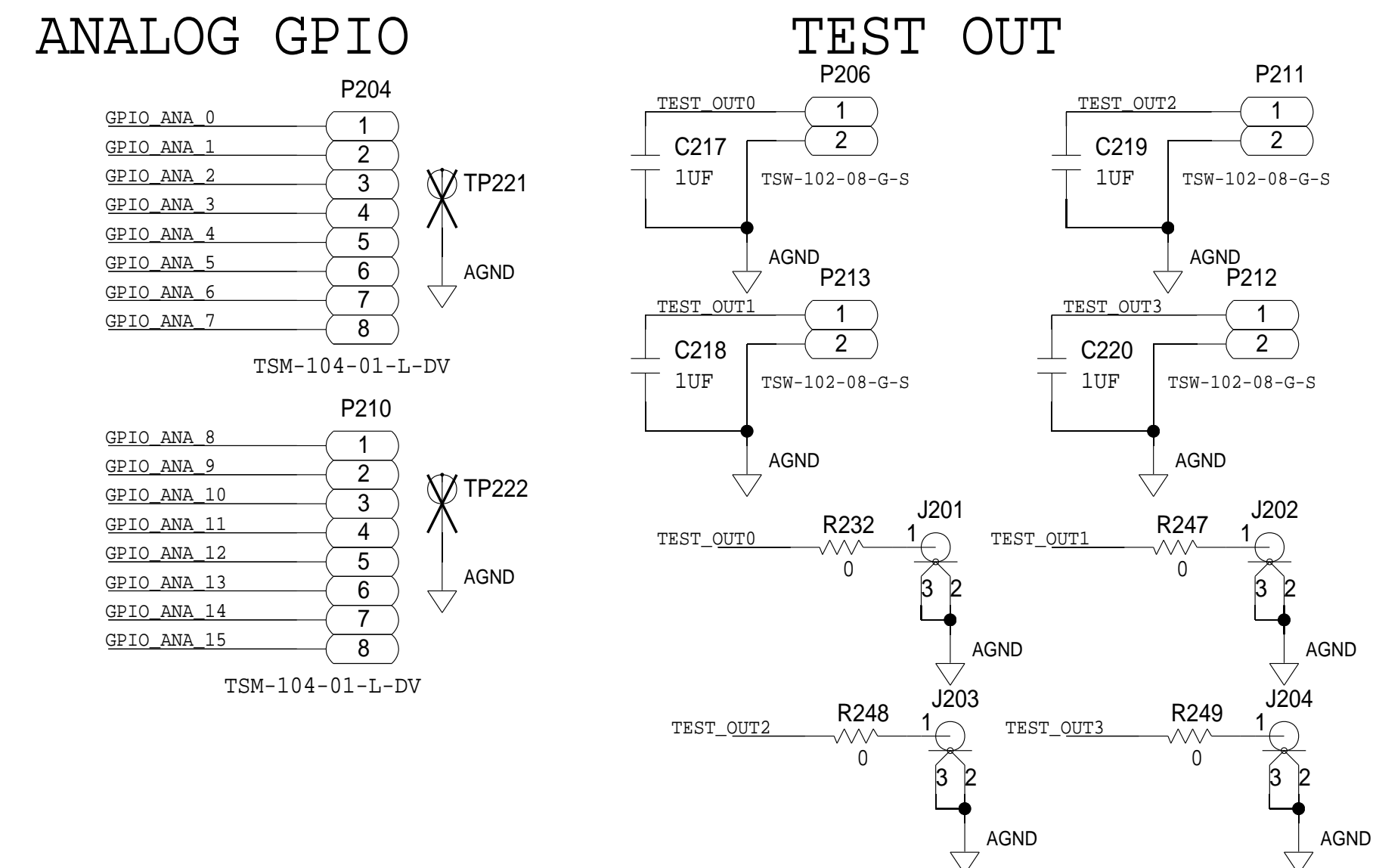
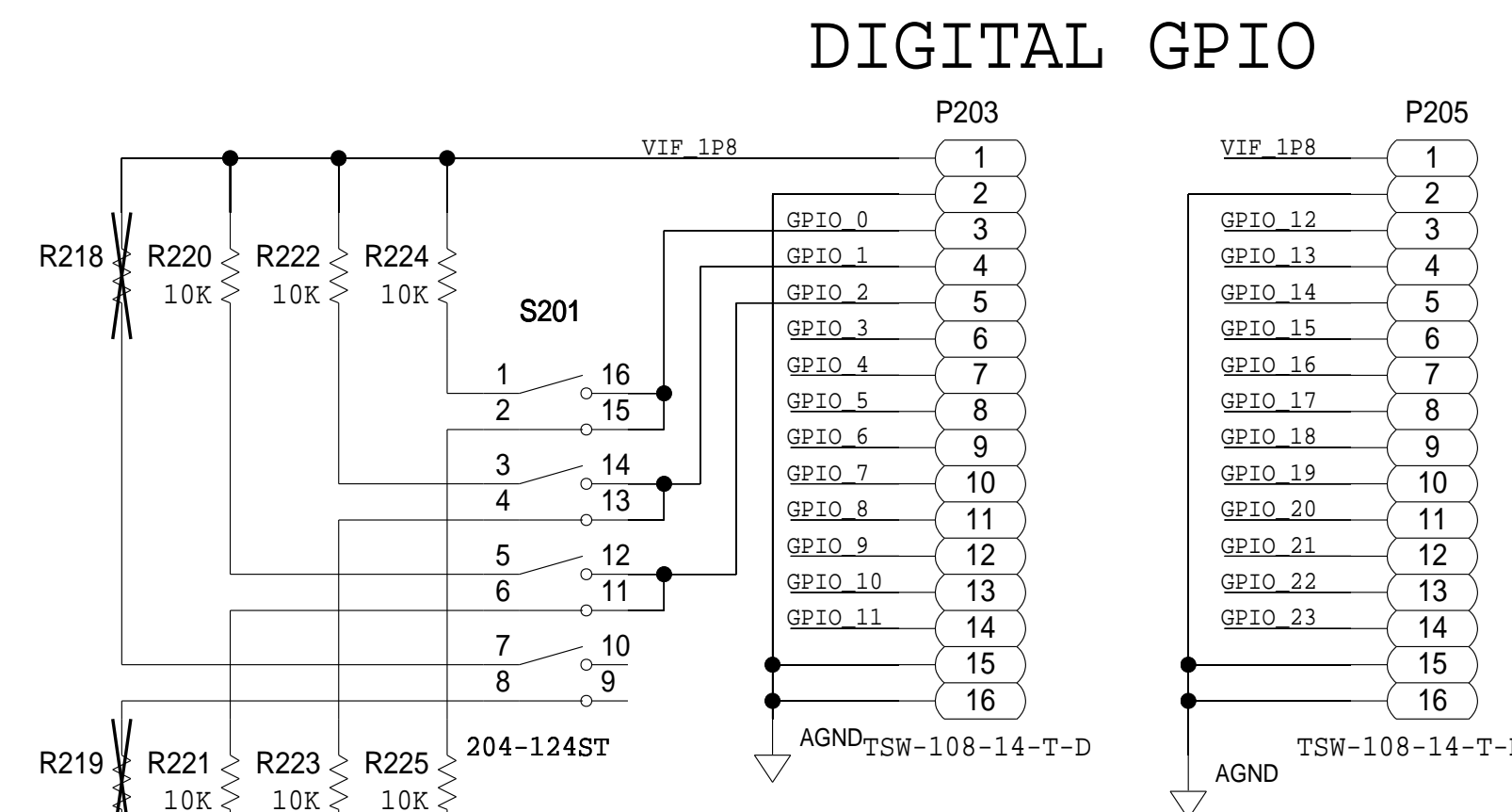
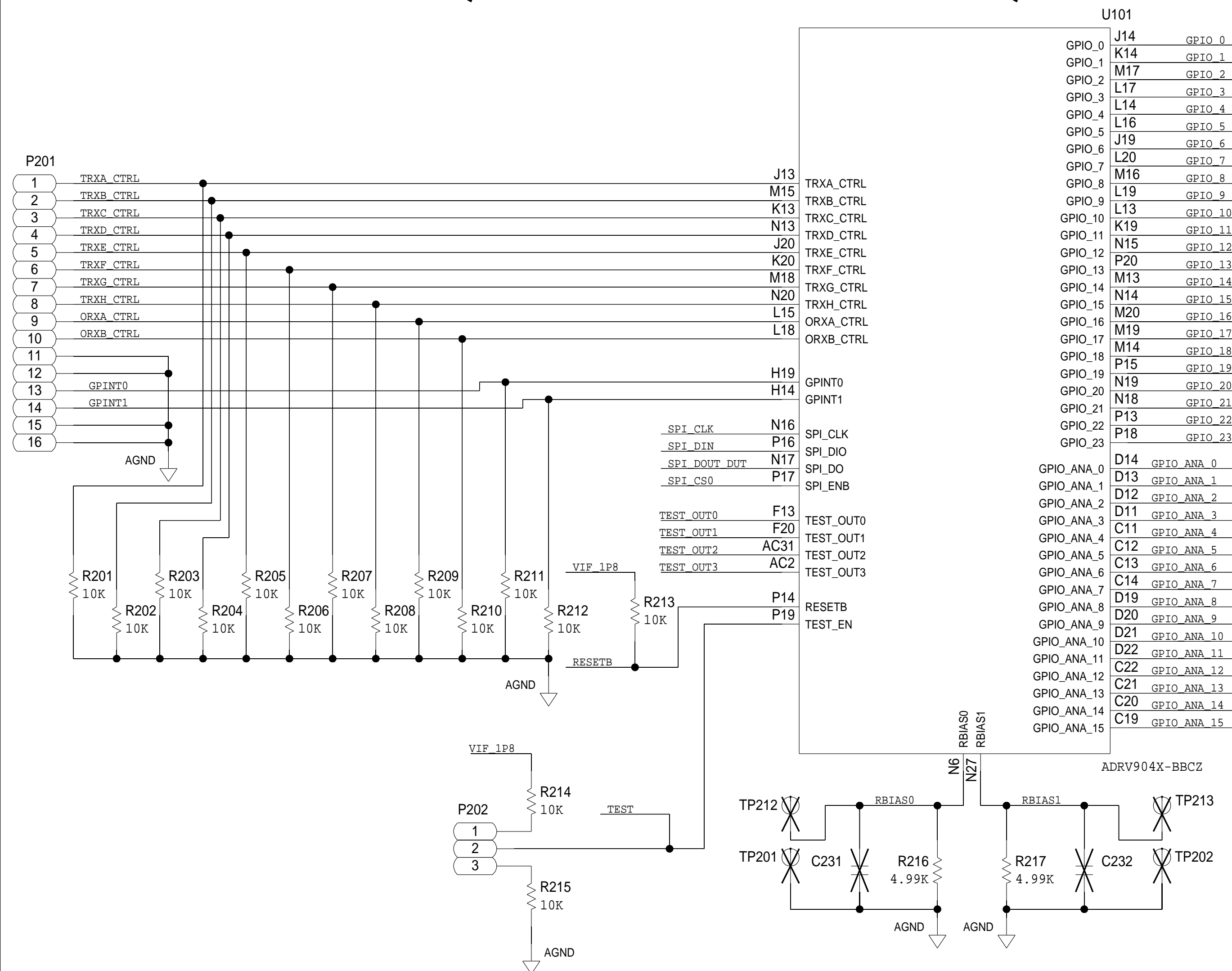



8		7		6		5		4		3		2		1	
D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					JUMPER TABLE			REVISIONS						
									REV	DESCRIPTION			DATE	APPROVED	
	JP#	ON	OFF												
	1														
	2														
	3														
	4														
	5														
						* SEE ASSEMBLY INSTRUCTIONS									
	RELAY CONTROL CHART														
CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR											
C															
B															
A															
P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER											
8	7	6	5	4	3	2	1								
TEMPLATE ENGINEER -		DATE		SCHEMATIC				 ANALOG DEV / CES							
HARDWARE SERVICES -				HW TYPE : Customer Evaluation Product(s): ADRV904X : N/A											
HARDWARE SYSTEMS -				PACKAGE : LeadCount-lead BodySize BGA_ED-family : Pitch-pitch StyleVendor Style											
TEST ENGINEER -				<User Define> <User Define> <User Define>											
COMPONENT ENGINEER -				MASTER PROJECT TEMPLATE TBD											
TEST PROCESS -				TESTER TEMPLATE no_template											
HARDWARE RELEASE -				DRAWING NO. 02_066795											
DESIGNER -				REV. C											
PTD ENGINEER <PTD_ENGINEER>				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES											
CHECKER -				TOLERANCES											
				DECIMALS FRACTIONS ANGLES X.XX +0.010 +-1/32 +-2 X.XXX +-0.005											
SIZE		SCALE		CODE ID NO.		SHEET 1		OF 19							
D		1:1		CodeID											

# DIGITAL / ANALOG I / O

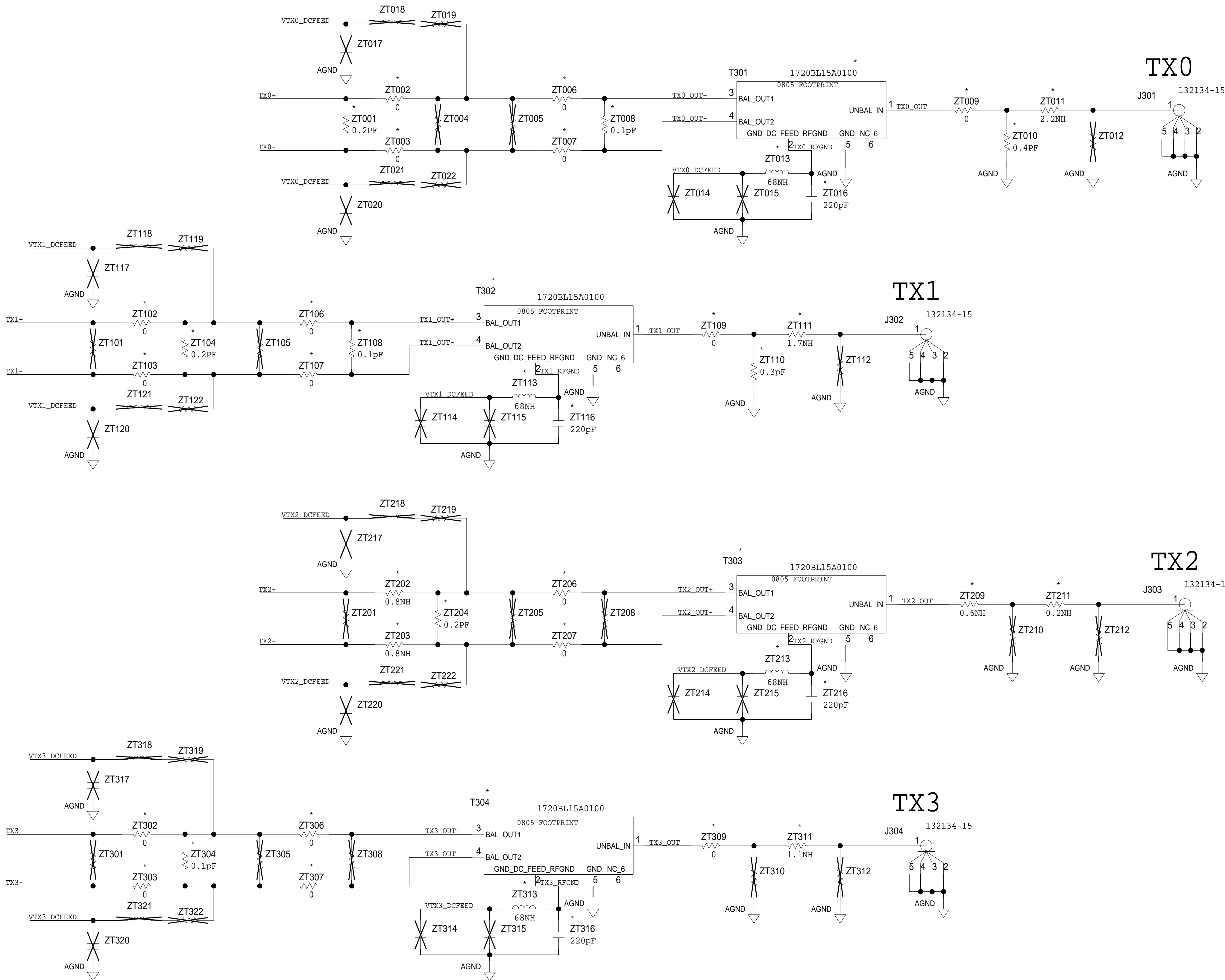
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



 <b>ANALOG DEVICES</b>	<h2 style="text-align: center;">SCHEMATIC</h2>				
	HW TYPE : Customer Evaluation Product(s) : ADRV904X : N/A				
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	PTD ENGINEER <b>&lt;PTD_ENGINEER&gt;</b>	SIZE <b>D</b>	SCALE <b>1:1</b>	SHEET <b>2</b>	OF <b>19</b>

# TRANSMITTER RF OUTPUT

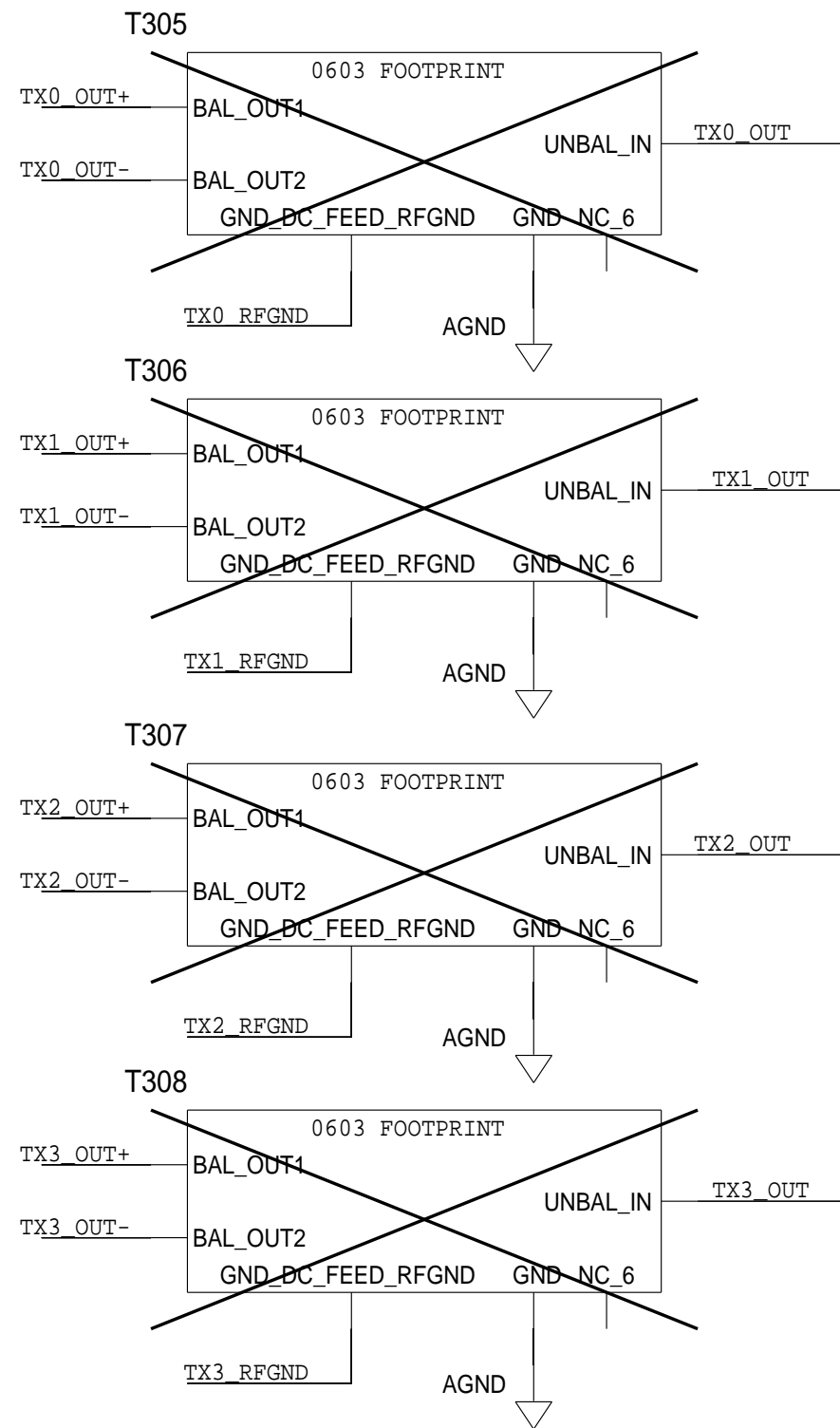
EVB MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;  
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.



U101		
B1	TX0-	
C1	TX0+	
G1	TX1-	
H1	TX1+	
R1	TX2-	
T1	TX2+	
Y1	TX3-	
AA1	TX3+	
C32	TX4-	
B32	TX4+	
H32	TX5-	
G32	TX5+	
T32	TX6-	
R32	TX6+	
AA32	TX7-	
Y32	TX7+	

ADRV904X-BBCZ

## OVERLAP TX BALUNS

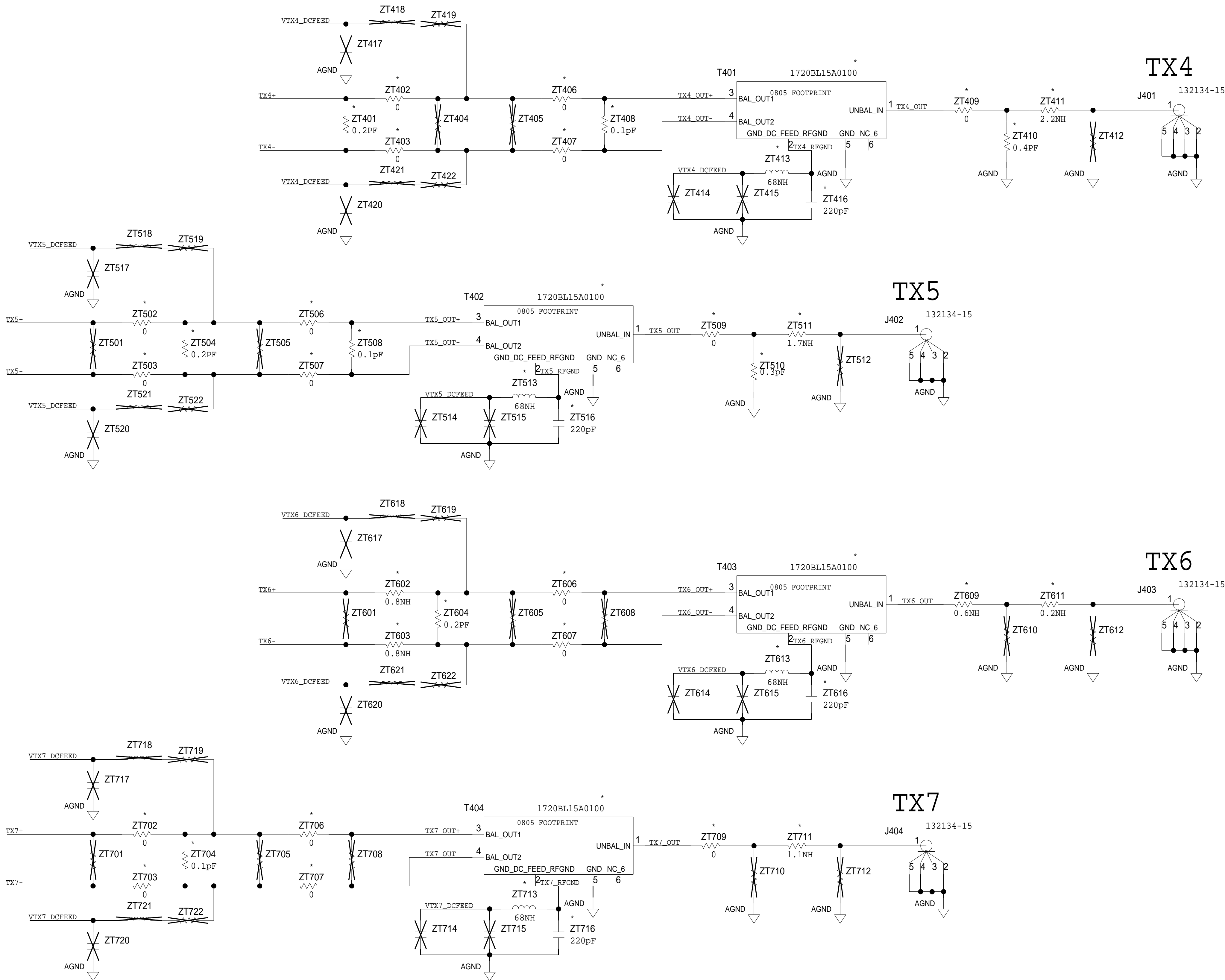


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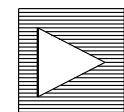
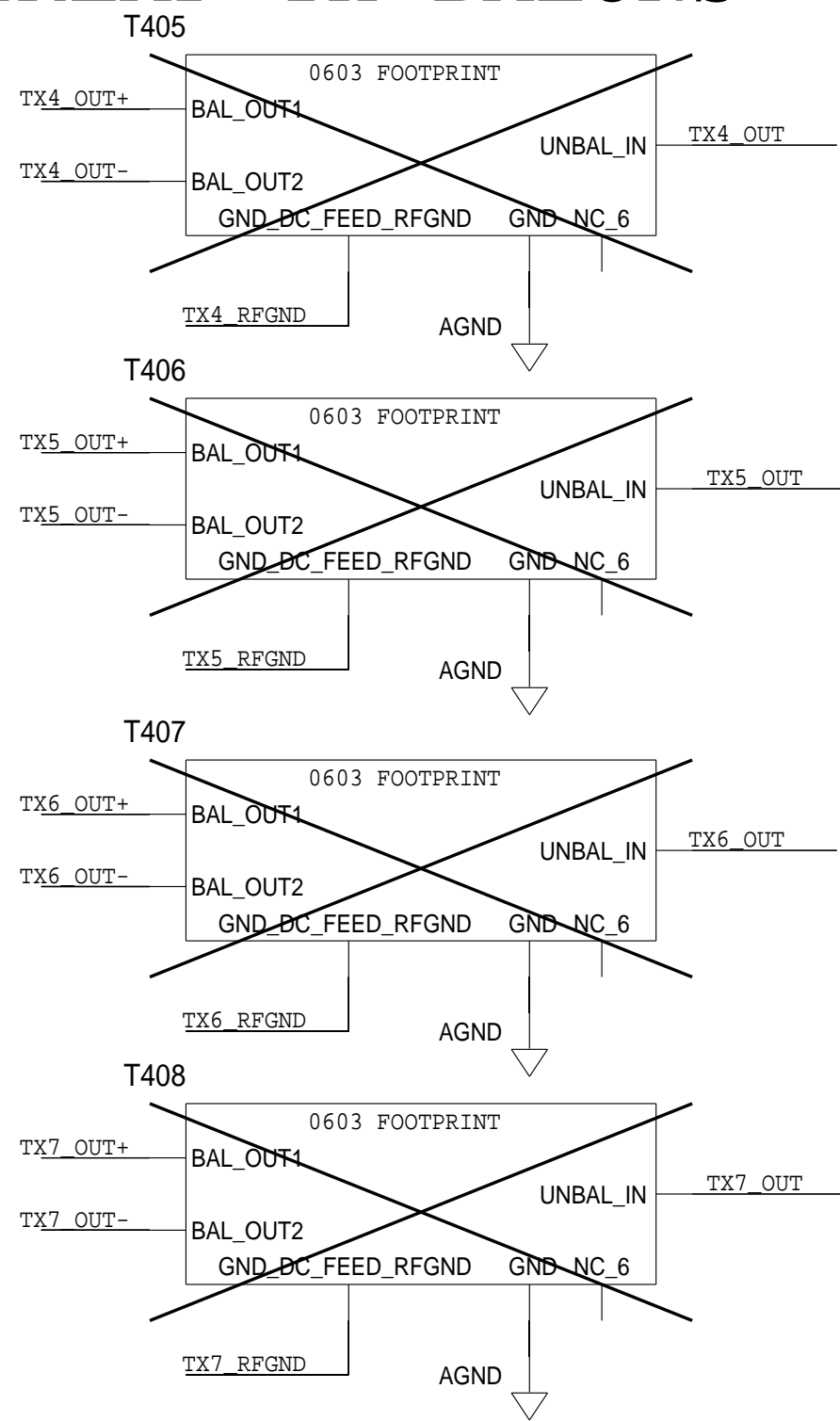
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV904X : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795	REV C	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 3 OF 19

# TRANSMITTER RF OUTPUT

EVB MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;  
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.



## OVERLAP TX BALUNS



ANALOG  
DEVICES

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### SCHEMATIC

HW TYPE : Customer Evaluation  
Product(s): ADRV904X  
: N/A

DESIGN VIEW  
<DESIGN\_VIEW>

PTD ENGINEER  
<PTD\_ENGINEER>

DRAWING NO.  
02\_066795

SIZE  
D

SCALE  
1:1

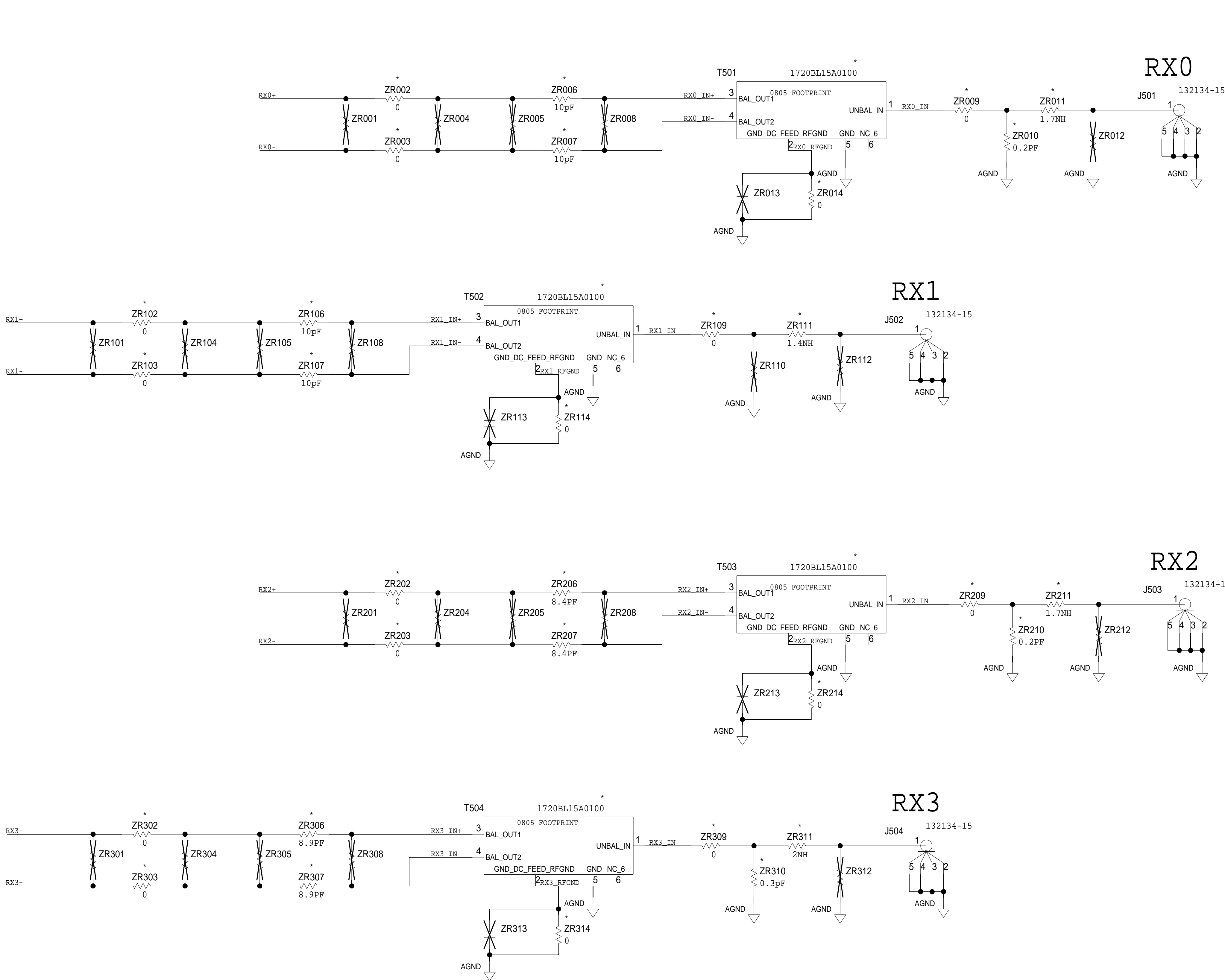
SHEET 4 OF 19

REV  
C

# RECEIVER RF INPUT

EVB MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;  
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

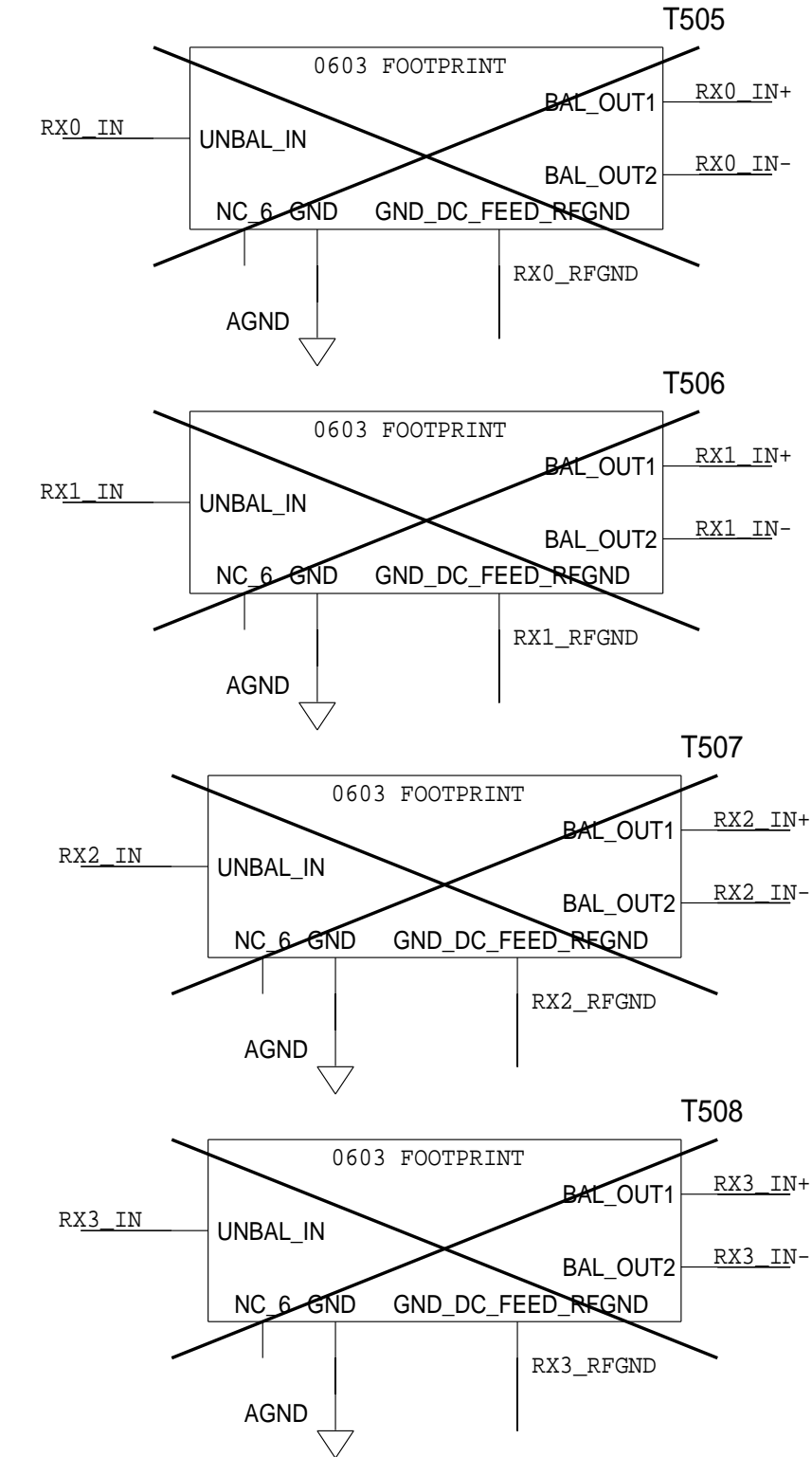
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



U101			
RX0-	D4	RX0N	
RX0+	C4	RX0P	
RX1-	J4	RX1N	
RX1+	H4	RX1P	
RX2-	P4	RX2N	
RX2+	N4	RX2P	
RX3-	W4	RX3N	
RX3+	V4	RX3P	
RX4-	C29	RX4N	
RX4+	D29	RX4P	
RX5-	H29	RX5N	
RX5+	J29	RX5P	
RX6-	N29	RX6N	
RX6+	P29	RX6P	
RX7-	V29	RX7N	
RX7+	W29	RX7P	

ADRV904X-BBCZ

## OVERLAP RX BALUNS

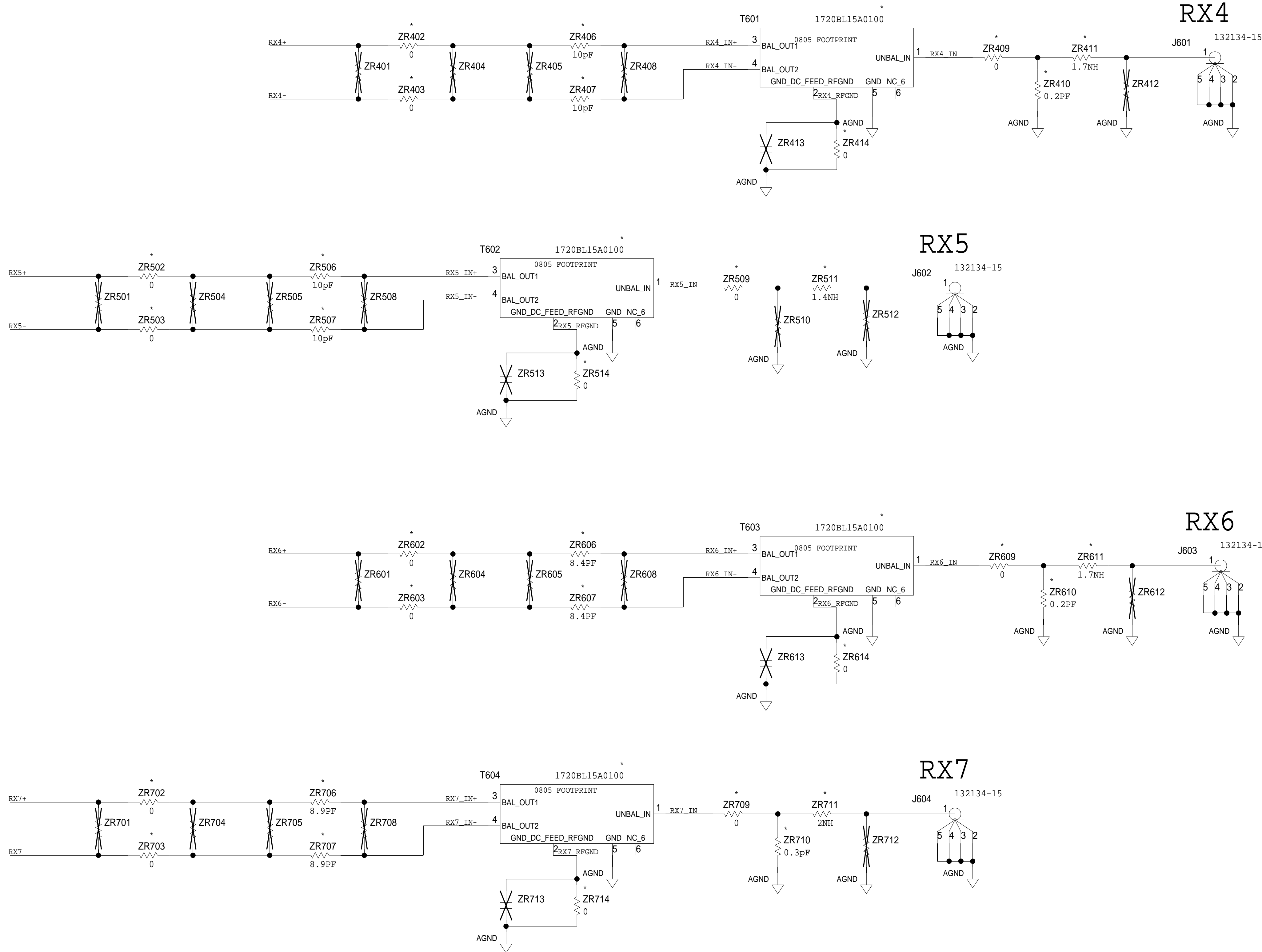


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV904X : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795	REV C	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 5 OF 19

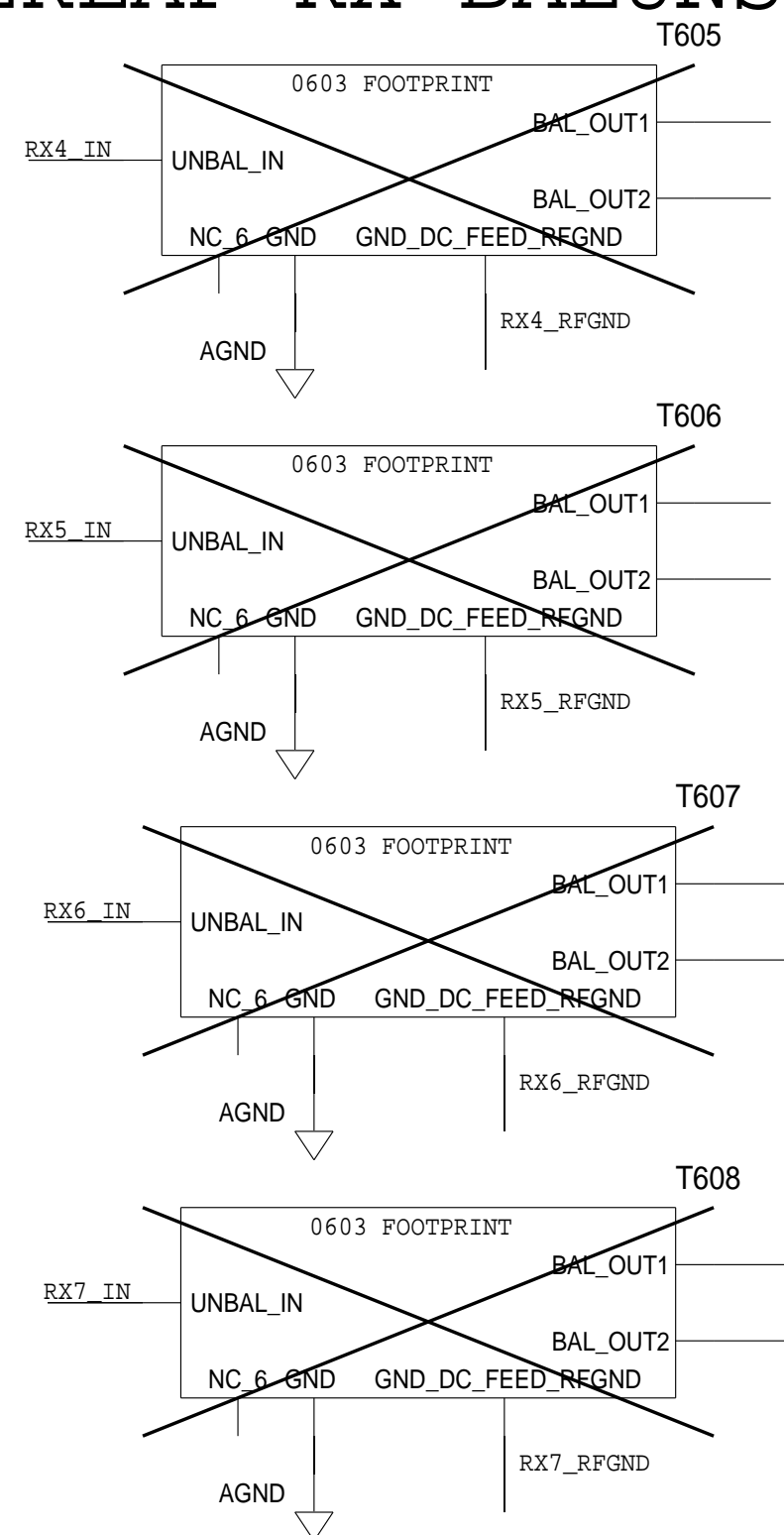
# RECEIVER RF INPUT

EVB MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;  
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



## OVERLAP RX BALUNS



### SCHEMATIC

HW TYPE : Customer Evaluation  
Product(s): ADRV904X  
: N/A

DESIGN VIEW  
<DESIGN\_VIEW>

DRAWING NO.  
02\_066795

REV  
C

PTD ENGINEER  
<PTD\_ENGINEER>

SIZE  
D

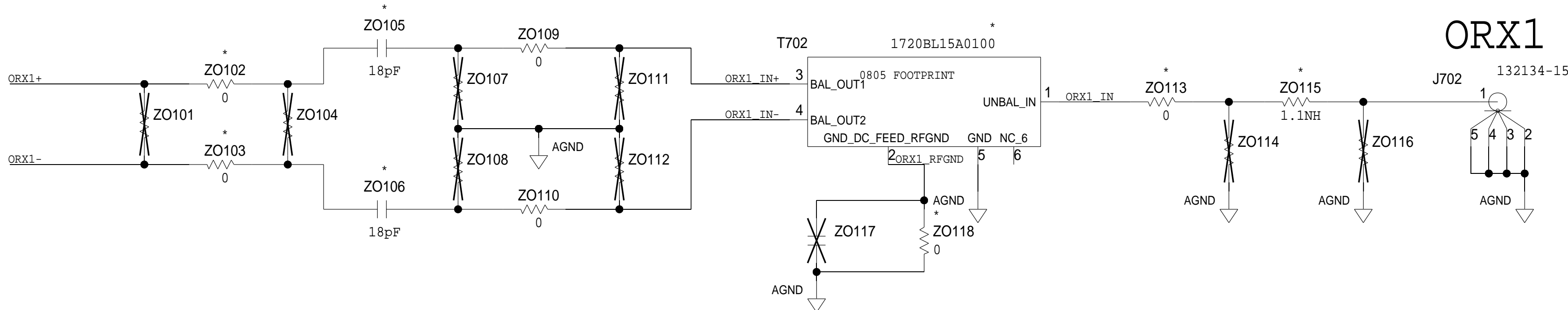
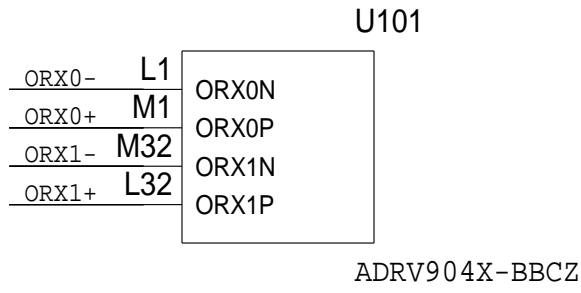
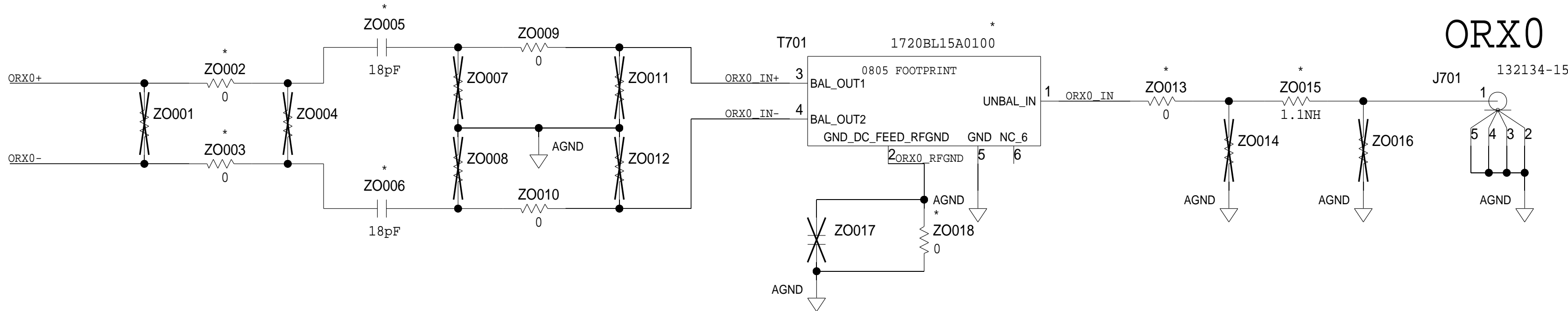
SCALE  
1:1

SHEET 6 OF 19

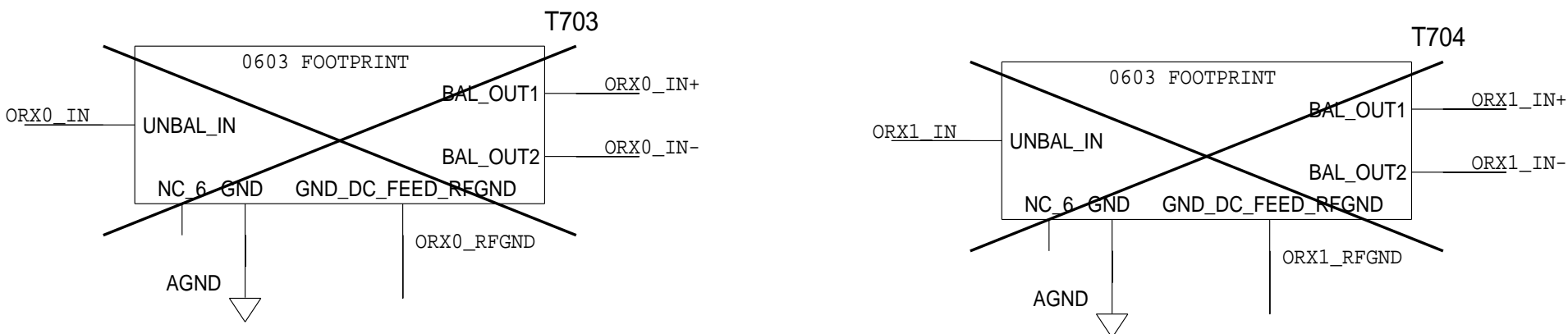
OBSERVATION RX RF INPUT

EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;  
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



OVERLAP ORX BALUNS



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SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV904X : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795		REV C
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 7 OF 19

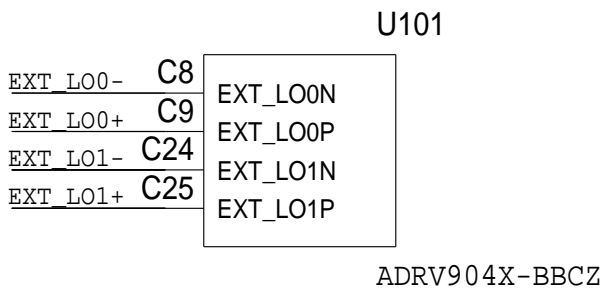
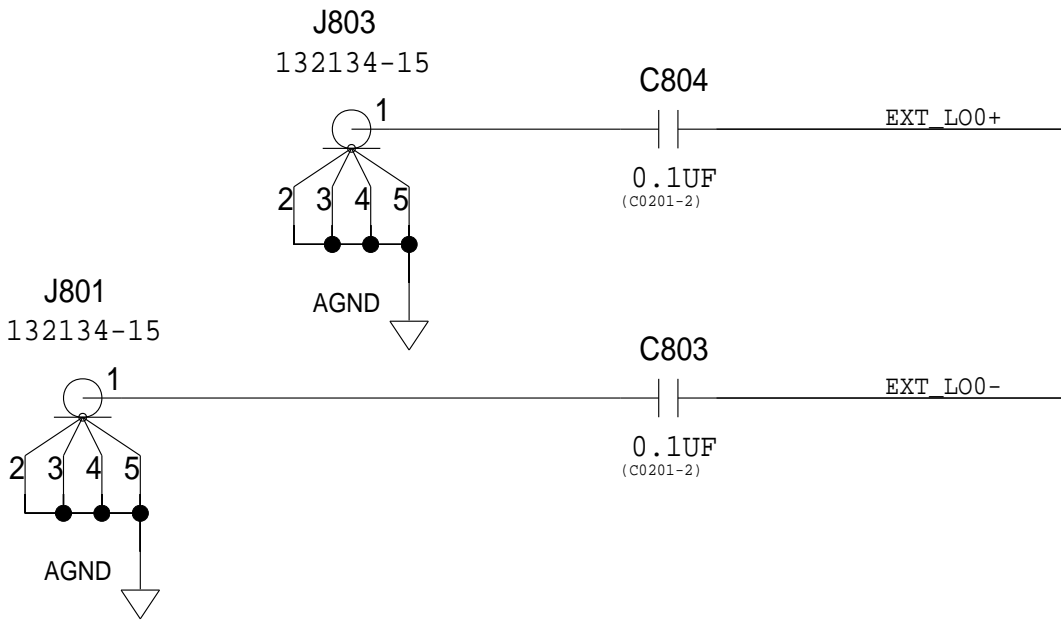


# EXTERNAL LO IN/OUT

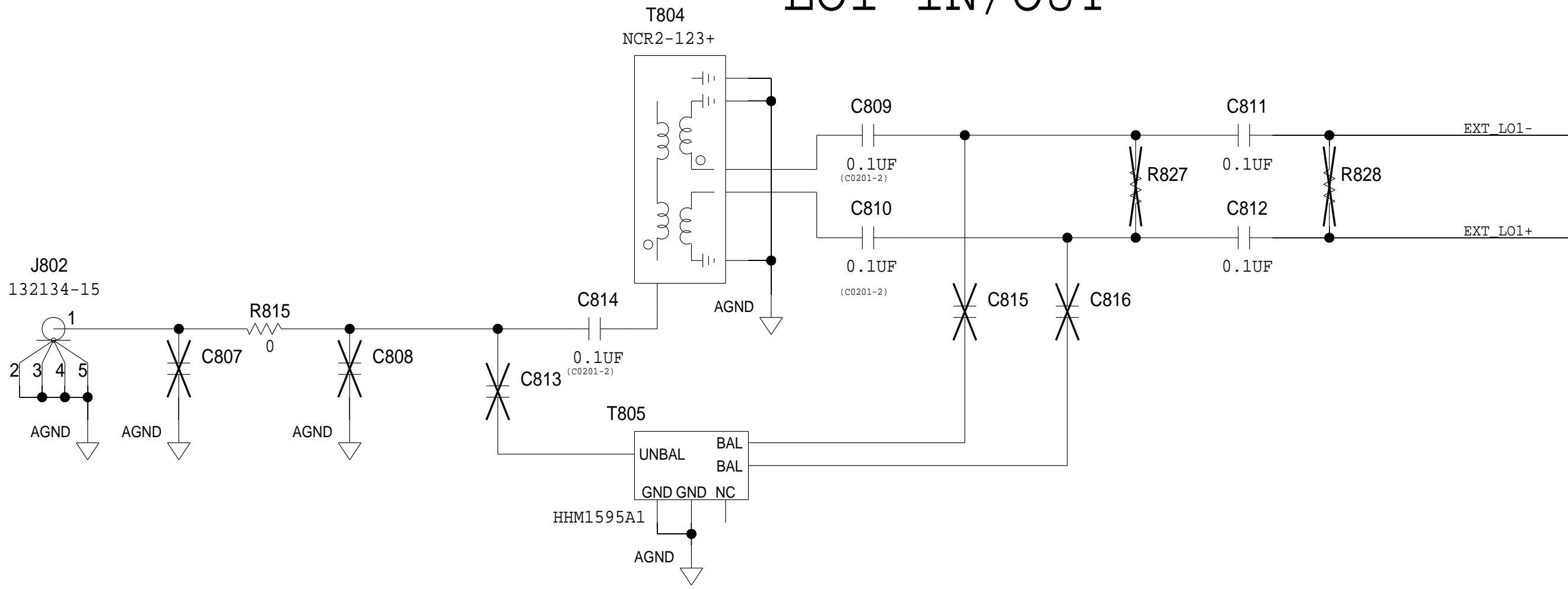
EVb MATCH HAS BEEN DESIGNED TO SUPPORT THE WIDEST MATCH POSSIBLE;  
MATCHING TOPOLOGY COULD BE REDUCED/SIMPLIFIED DEPENDING ON THE SYSTEM REQUIREMENTS.

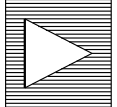
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

## LO0 IN/OUT



## LO1 IN/OUT

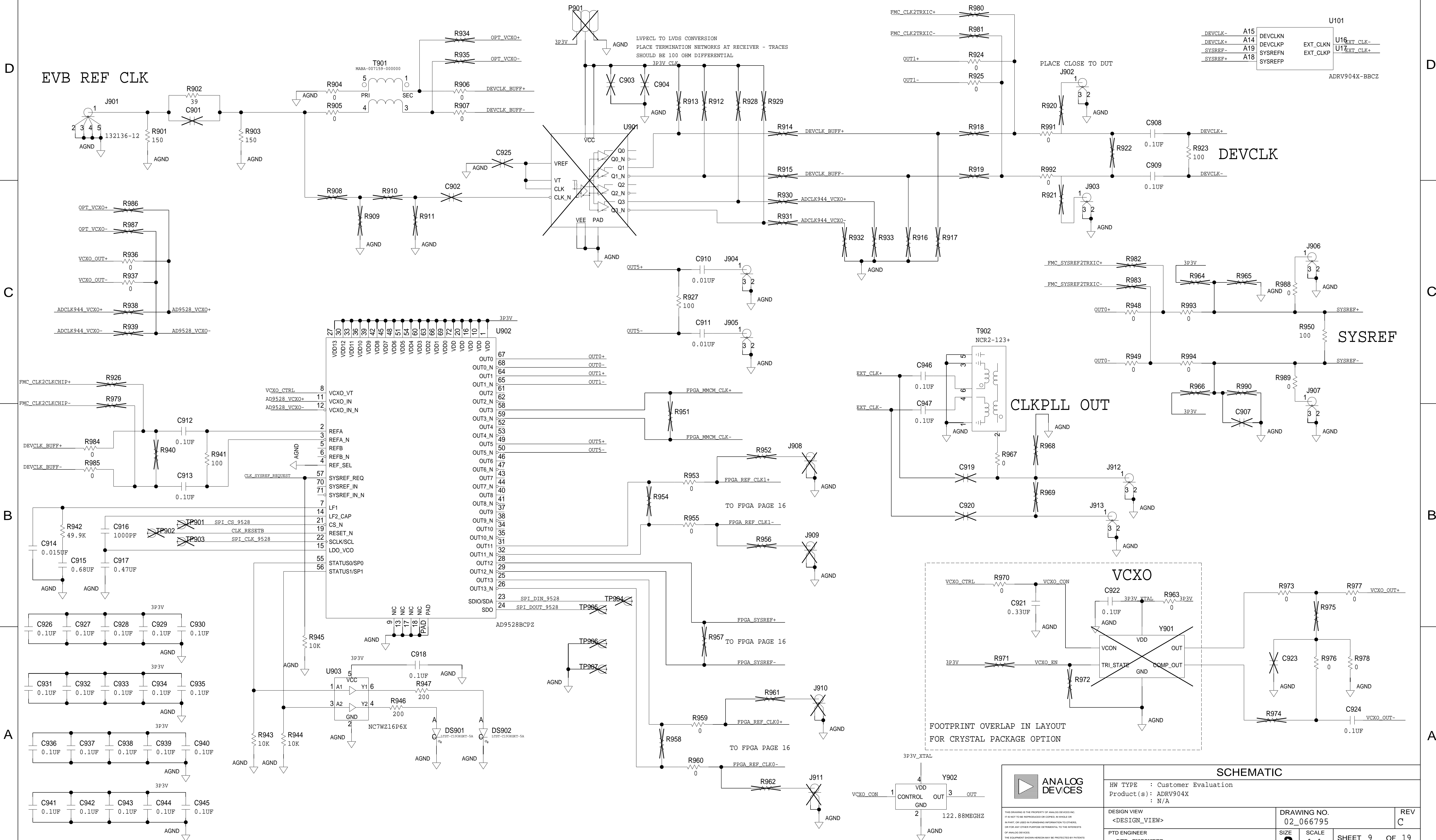


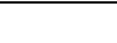
 ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s): ADRV904X : N/A			
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795		REV C
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 8 OF 19



# CLOCK GENERATION/DISTRIBUTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



 <b>ANALOG DEVICES</b>	<h1 style="text-align: center;">SCHEMATIC</h1>				
	HW TYPE : Customer Evaluation Product(s) : ADRV904X : N/A				
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PTD ENGINEER <b>&lt;PTD_ENGINEER&gt;</b>	SIZE <b>D</b>	SCALE 1:1	SHEET 9	OF 19	

POWER SUPPLY

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

3P3V

U1001 LT8625SP-1

1P8V, 1.0V, 0.8V POWER SUPPLY DUT

VIF\_1P8

SCHEMATIC

HW TYPE : Customer Evaluation  
Product(s): ADRV904X : N/A

DESIGN VIEW  
<DESIGN\_VIEW>

PTD ENGINEER  
<PTD\_ENGINEER>

DRAWING NO.  
02\_066795

REV  
C

SIZE  
D

SCALE  
1:1

SHEET 10 OF 19

**POWER SUPPLY**

**REVISIONS**

REV	DESCRIPTION	DATE	APPROVED

**3P3V**

**VIF\_1P8**

**1.8V, POWER E-EVB**

**1.0V, SUPPLY DUT**

**0.8V**

**SCHEMATIC**

HW TYPE : Customer Evaluation  
Product(s) : ADRV904X : N/A

DESIGN VIEW  
<DESIGN\_VIEW>

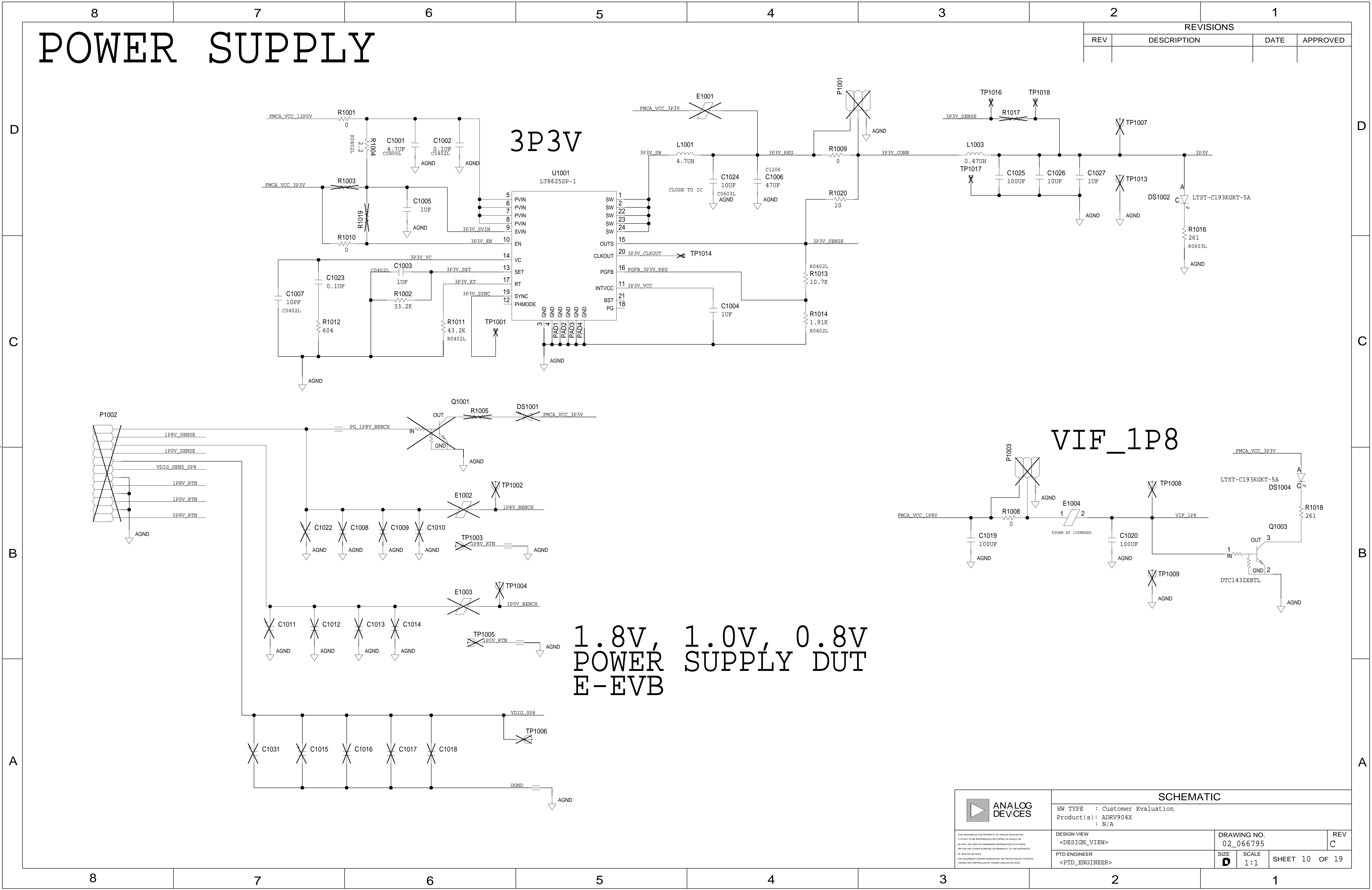
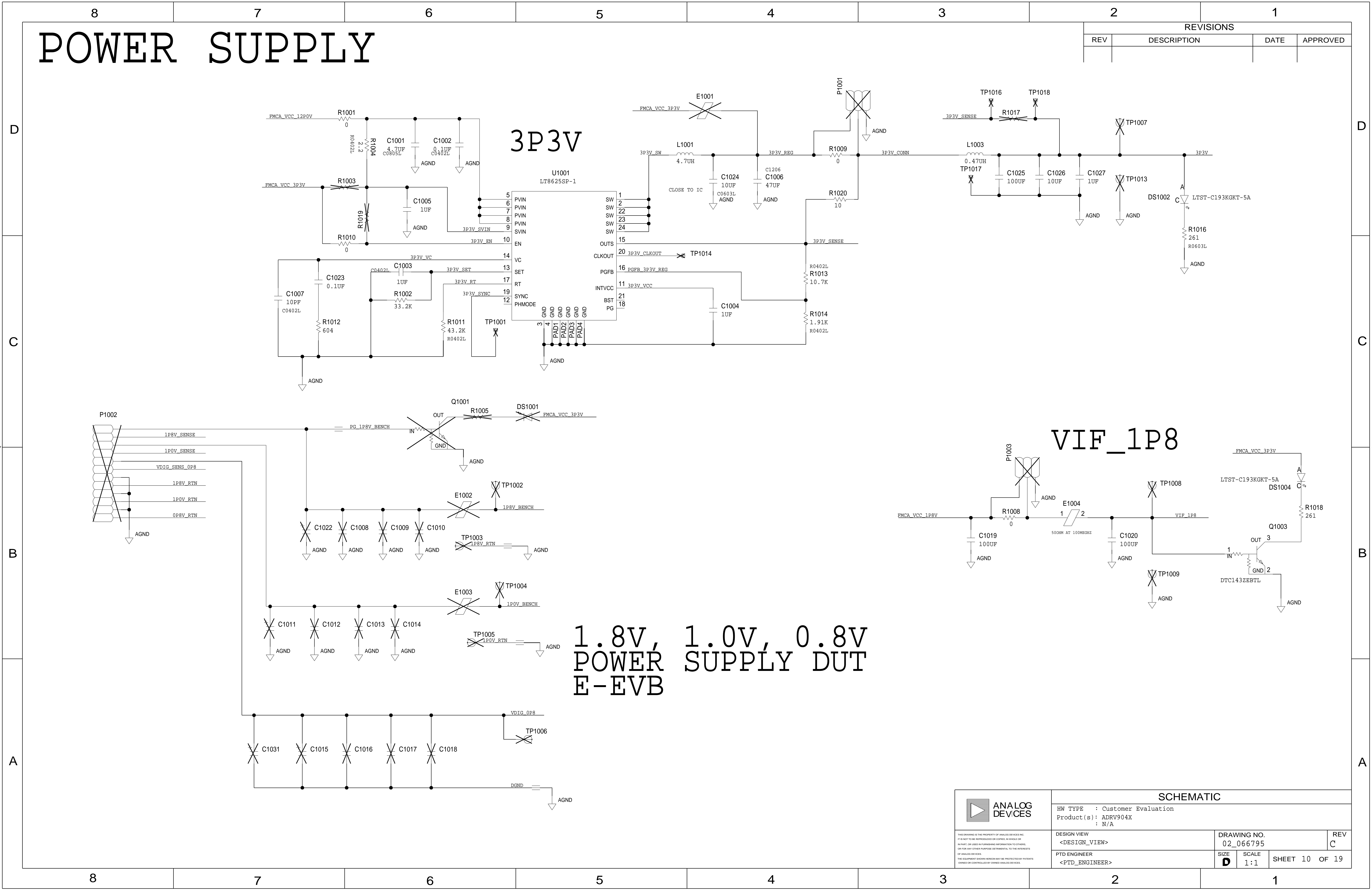
PTD ENGINEER  
<PTD\_ENGINEER>

DRAWING NO.  
02\_066795

SCALE  
1:1

SHEET 10 OF 19

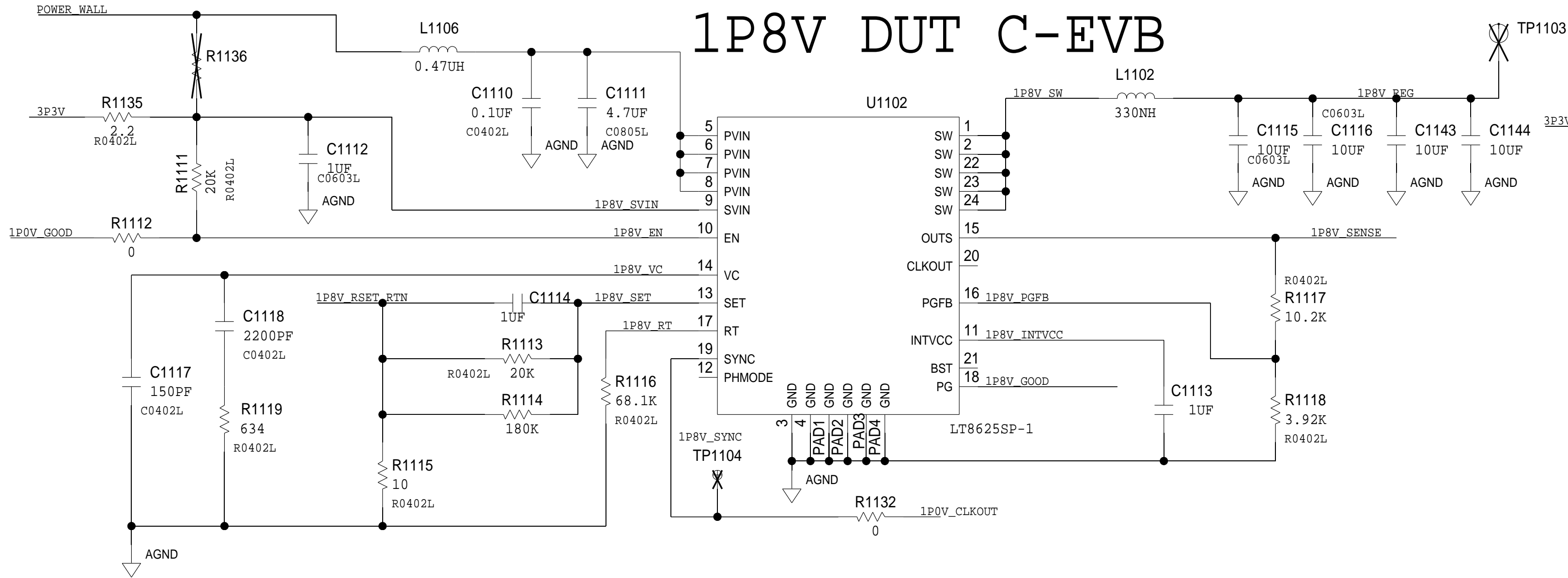
REV C

[illegible][illegible]

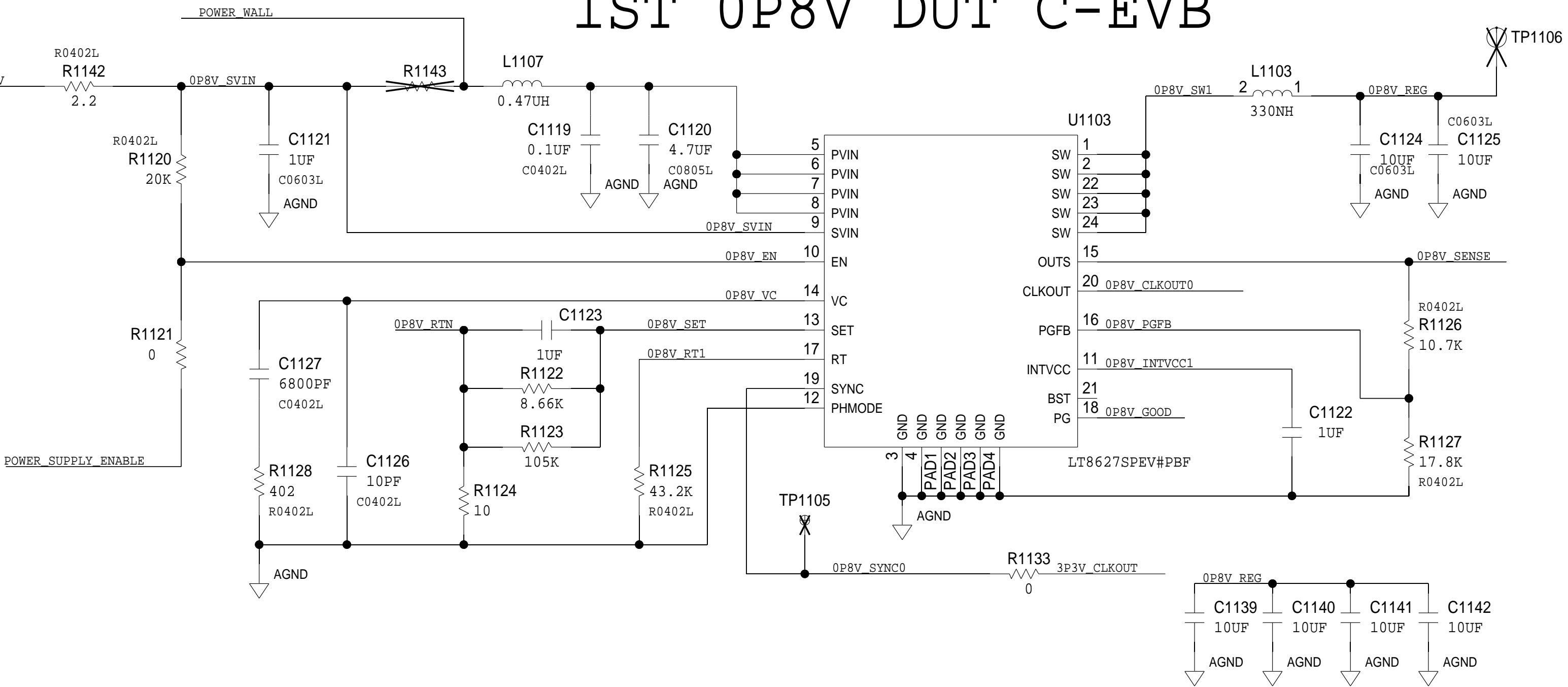
POWER SILENT SWITCHER

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

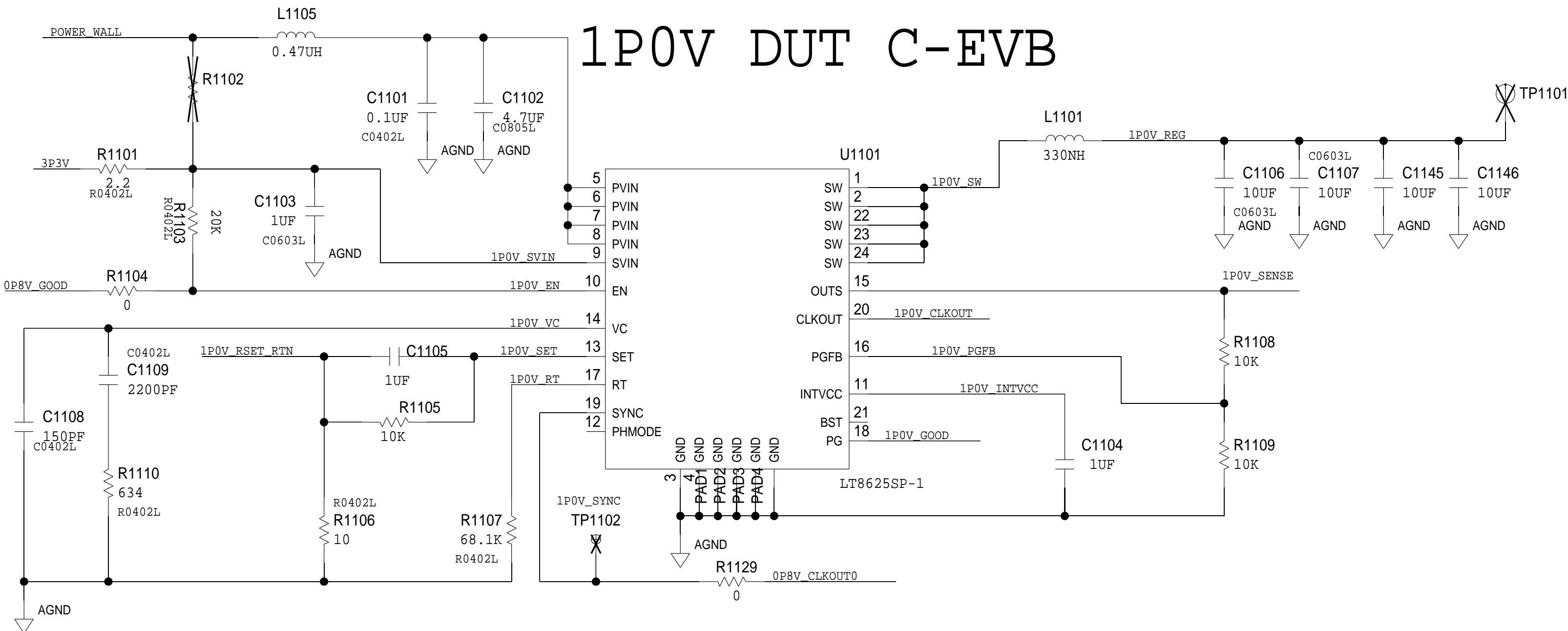
1P8V DUT C-EVB



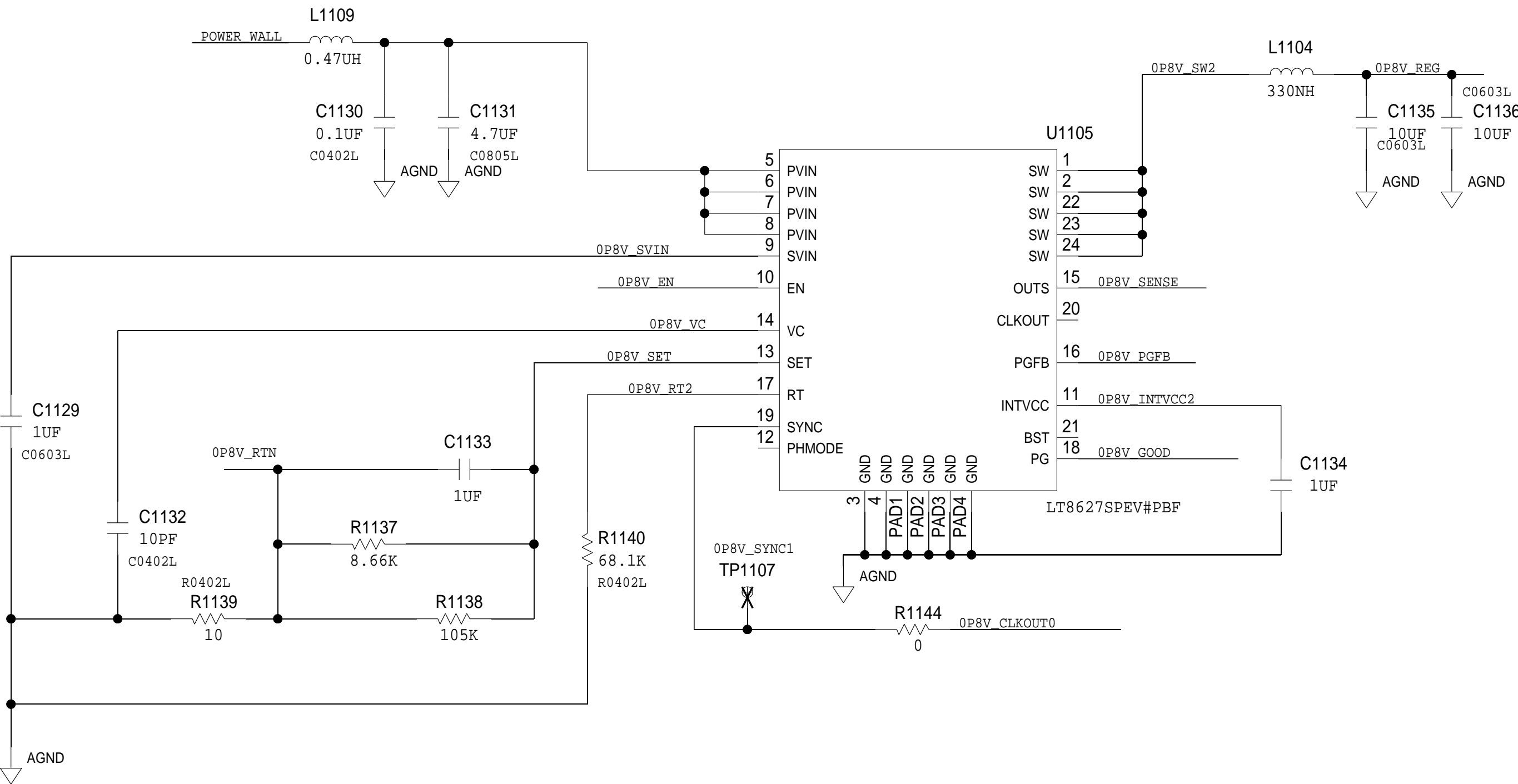
1ST 0P8V DUT C-EVB



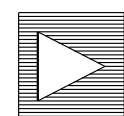
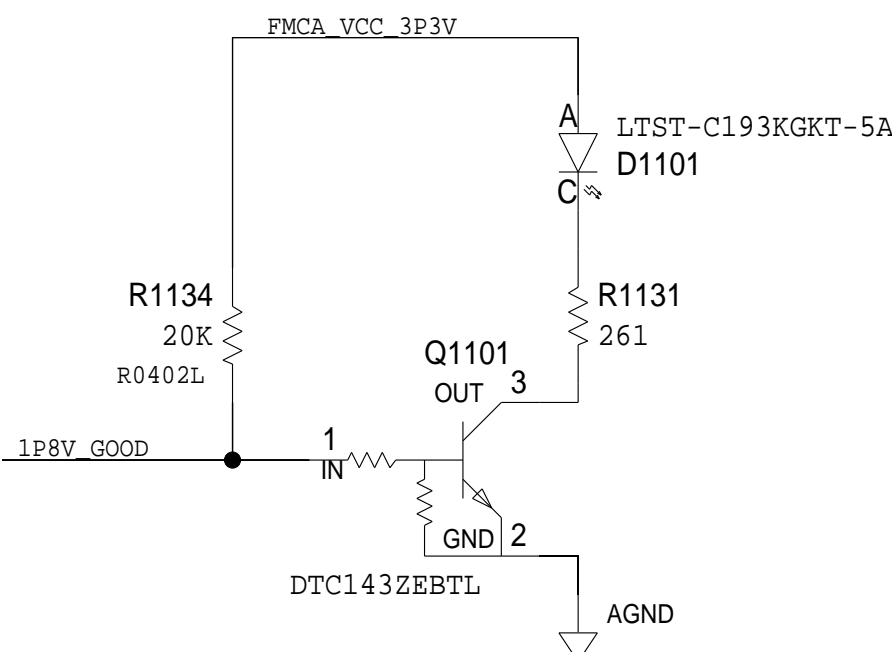
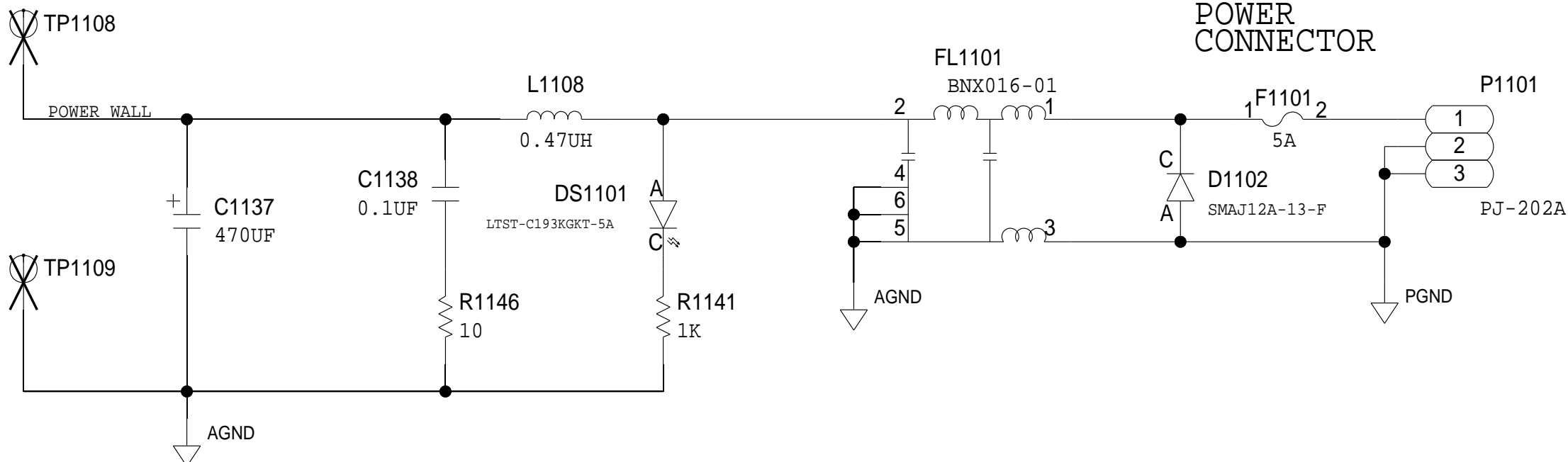
1P0V DUT C-EVB



2ND 0P8V DUT C-EVB



6-12VDC, 5A MAX  
POWER CONNECTOR



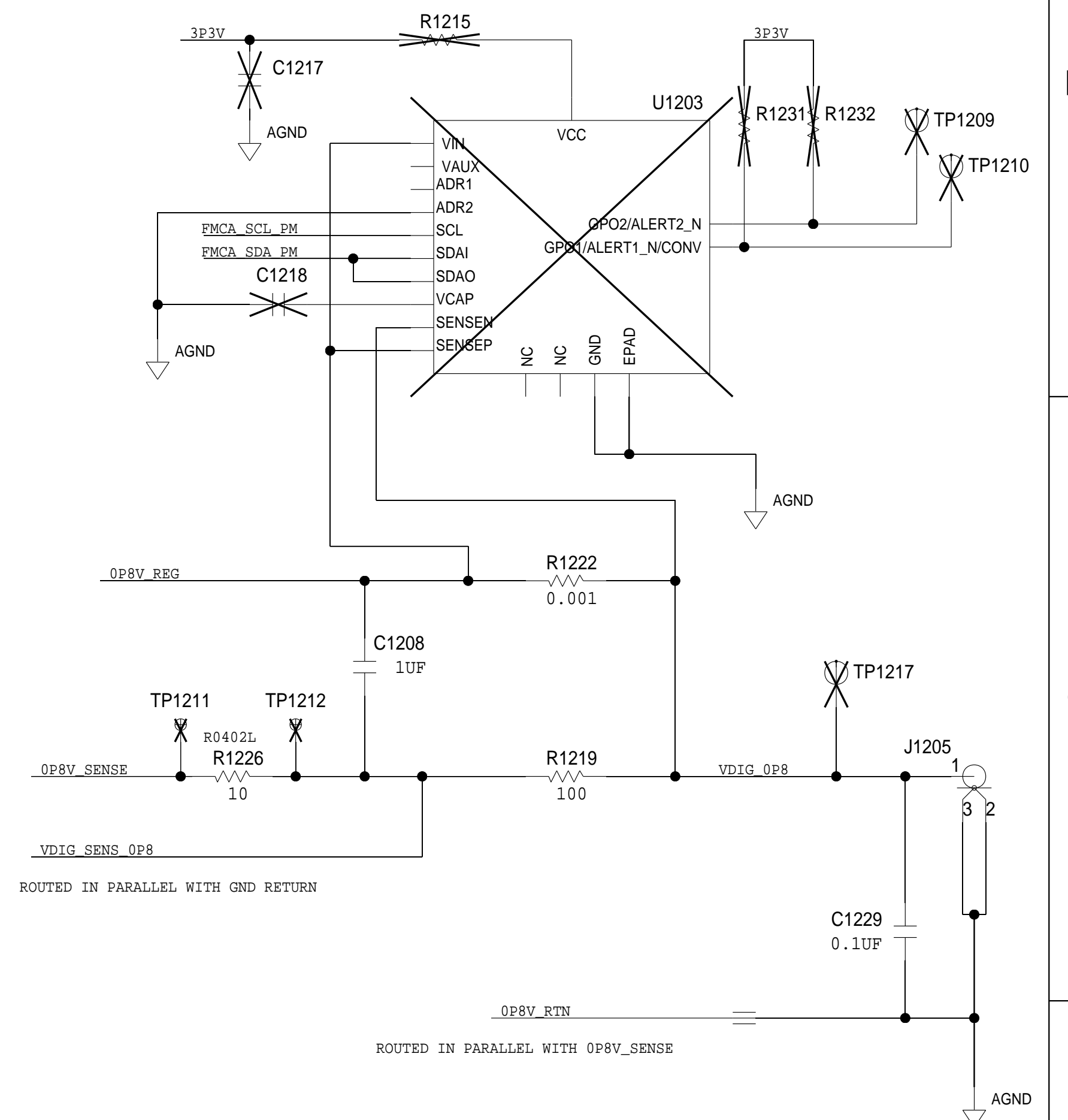
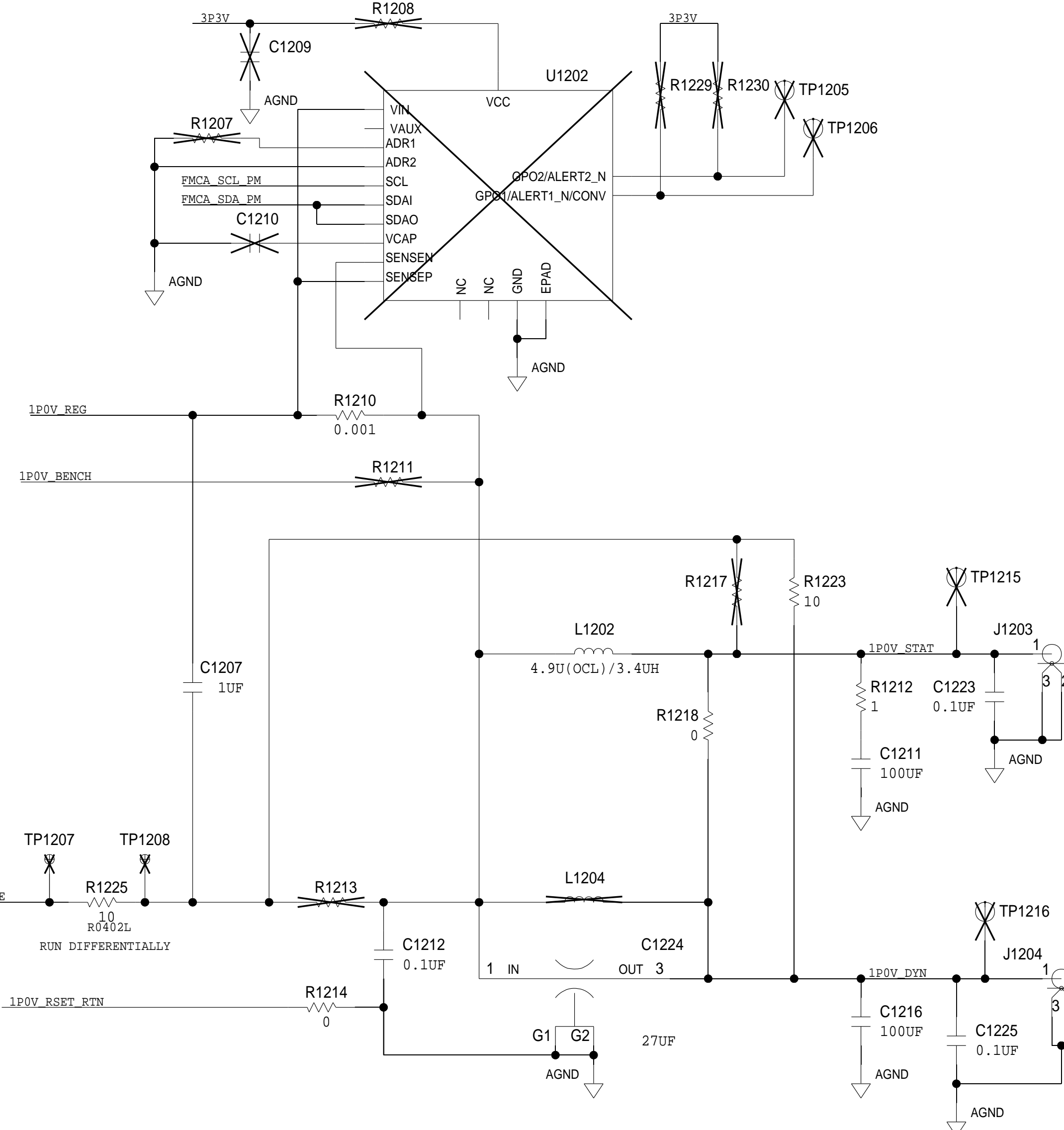
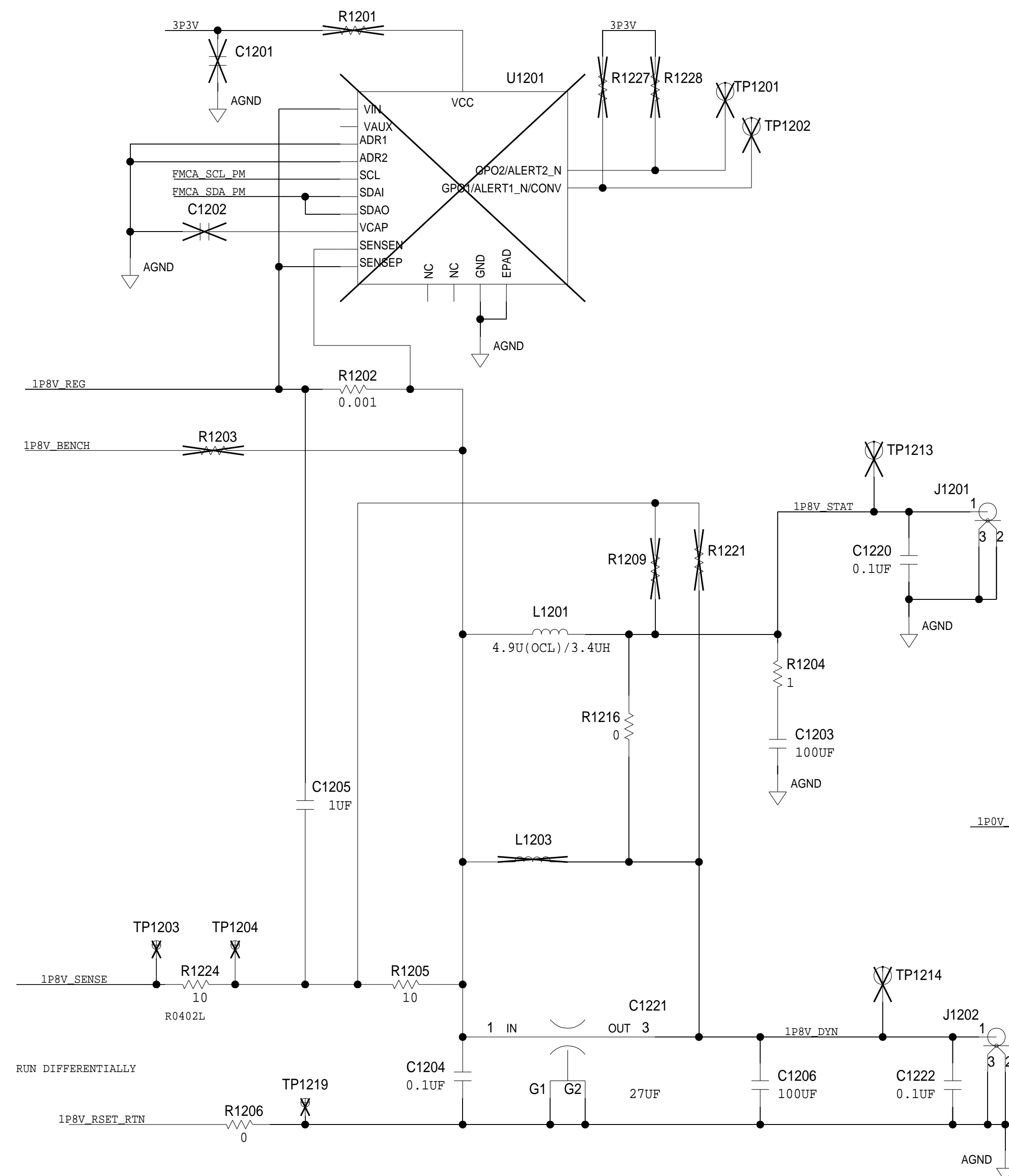
ANALOG  
DEVICES

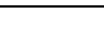
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SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV904X : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795	REV C	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 11 OF 19

# POWER MEASUREMENT & FILTERING

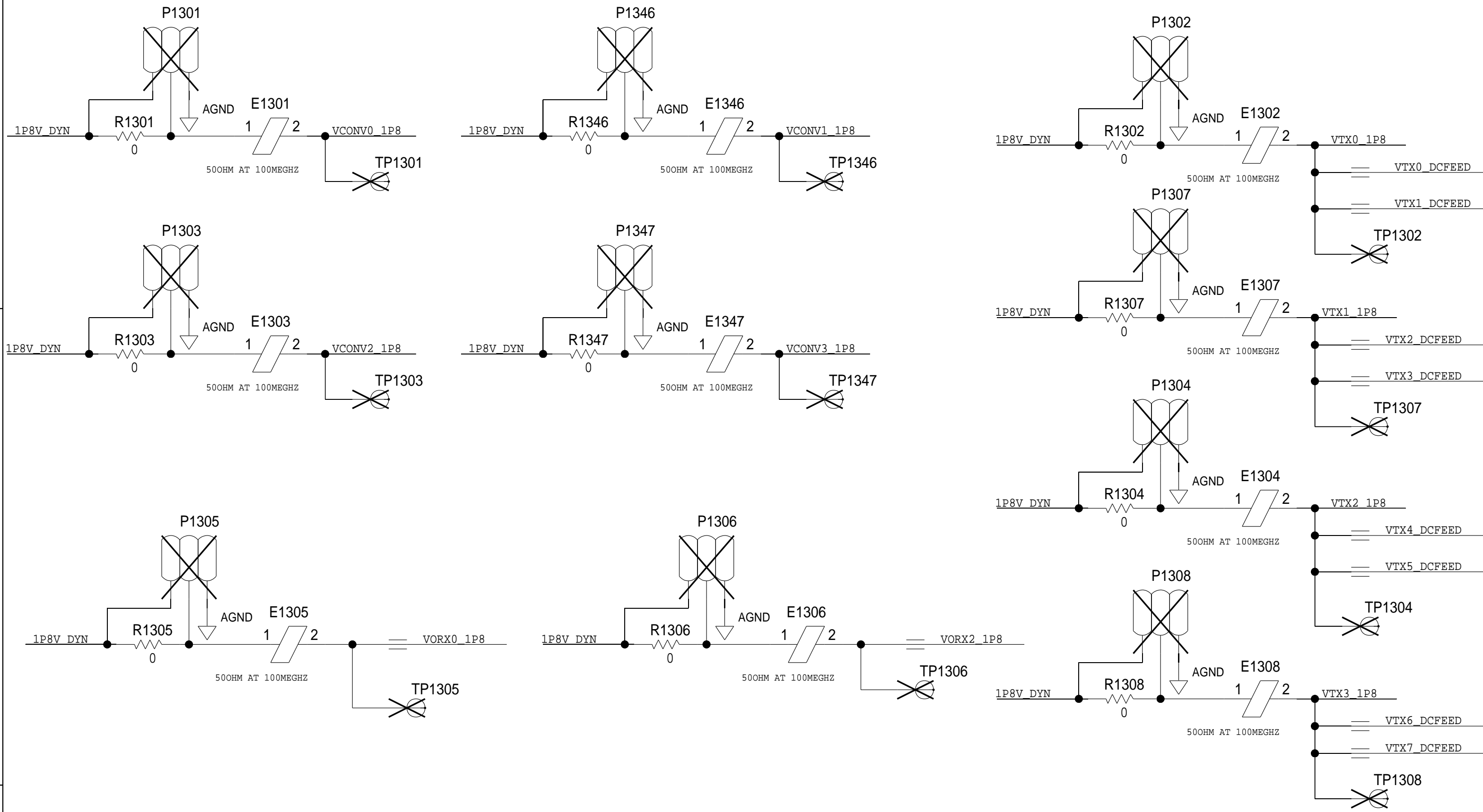
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



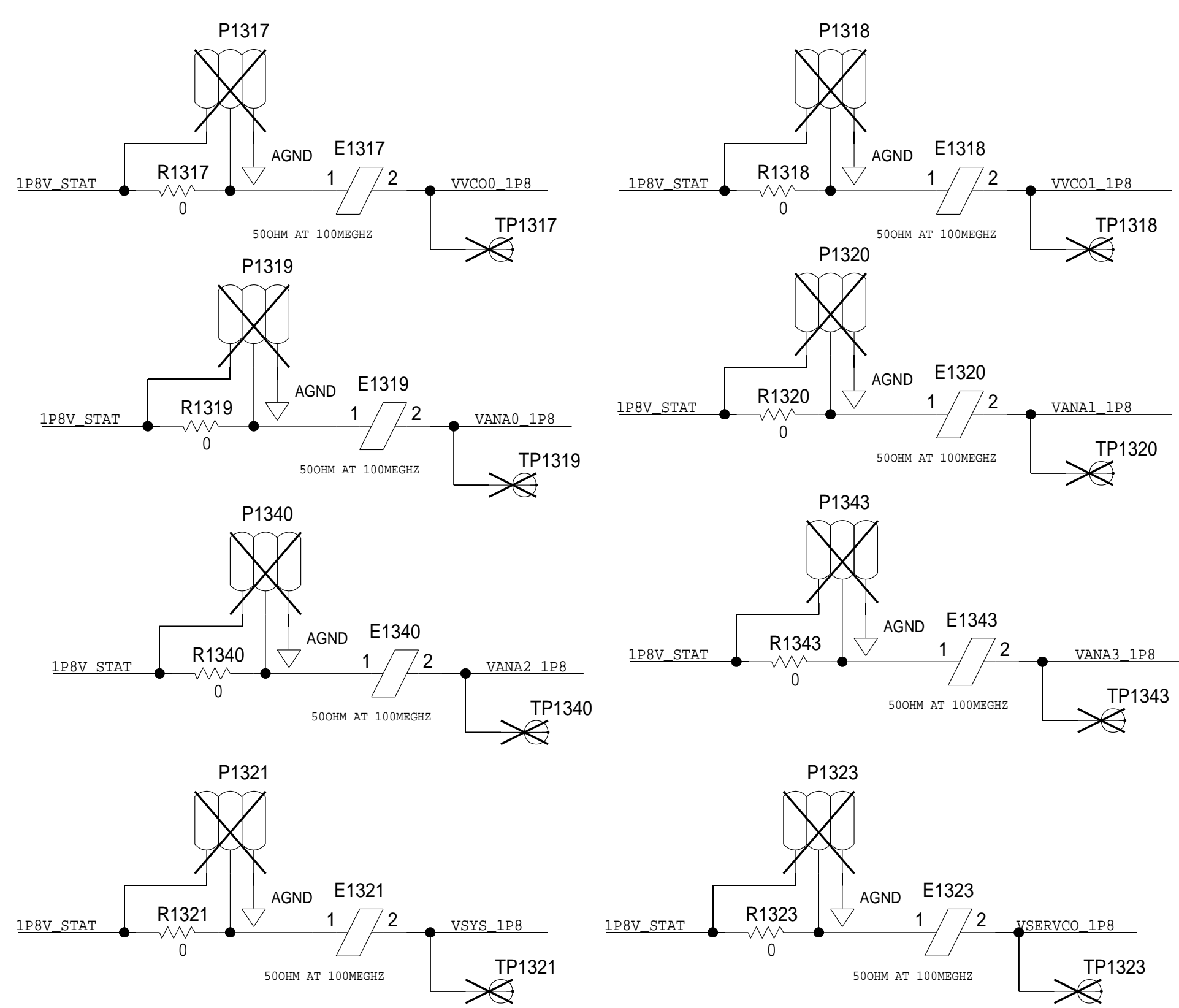
 <b>ANALOG DEVICES</b>		<h1 style="text-align: center;">SCHEMATIC</h1>				
		HW TYPE : Customer Evaluation Product(s) : ADRV904X : N/A				
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		PTD ENGINEER <b>&lt;PTD_ENGINEER&gt;</b>		SIZE <b>D</b>	SCALE <b>1:1</b>	SHEET <b>12</b> OF <b>19</b>

# POWER DISTRIBUTION

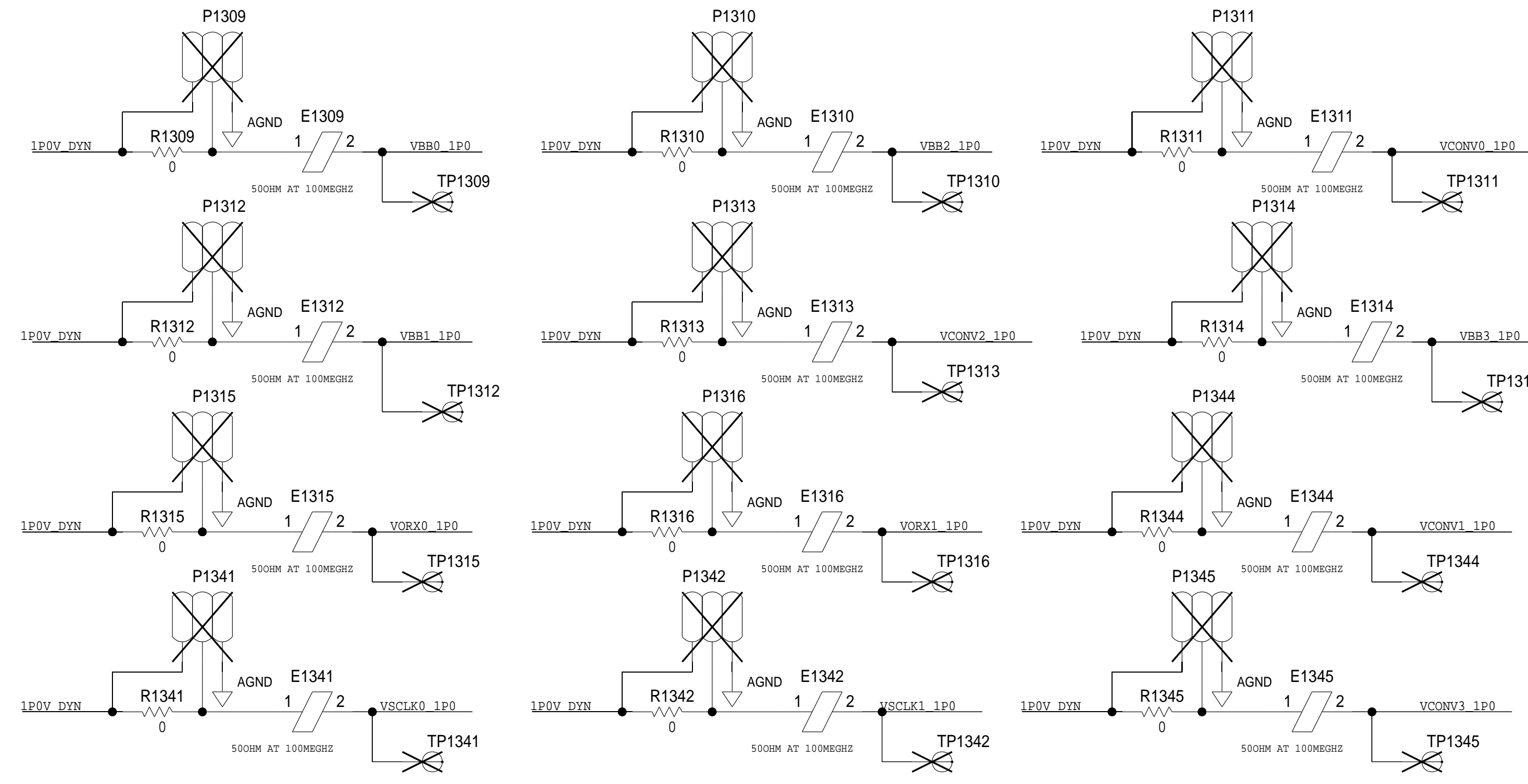
## 1.8V ANALOG DYNAMIC SUPPLIES



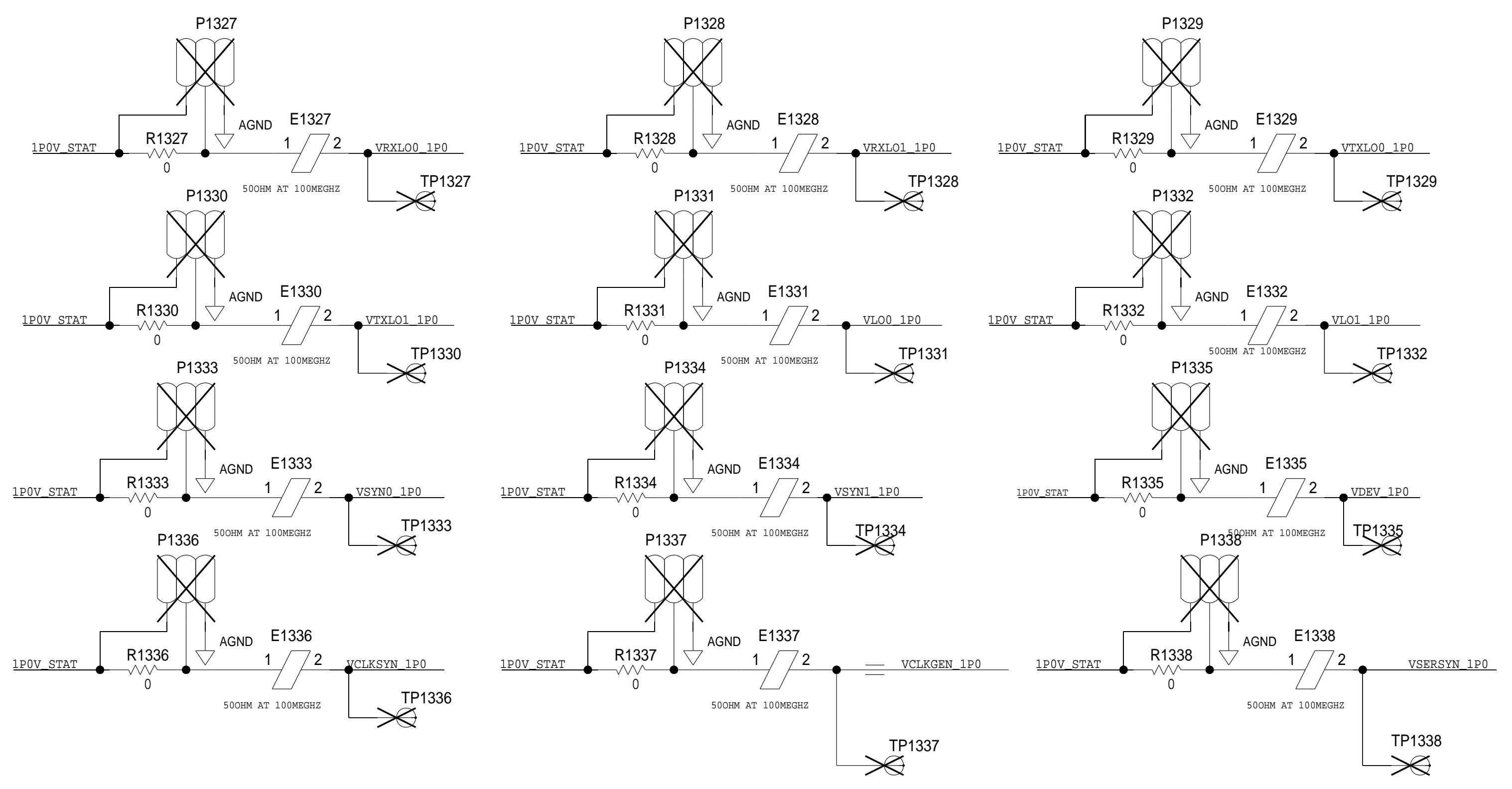
## 1.8V ANALOG STATIC SUPPLIES



## 1.0V ANALOG DYNAMIC SUPPLIES



## 1.0V ANALOG STATIC SUPPLIES



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SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV904X : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795	REV C	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 13 OF 19



POWER INPUT

1.8V CAPS

0.8V CAPS

1.0V CAPS

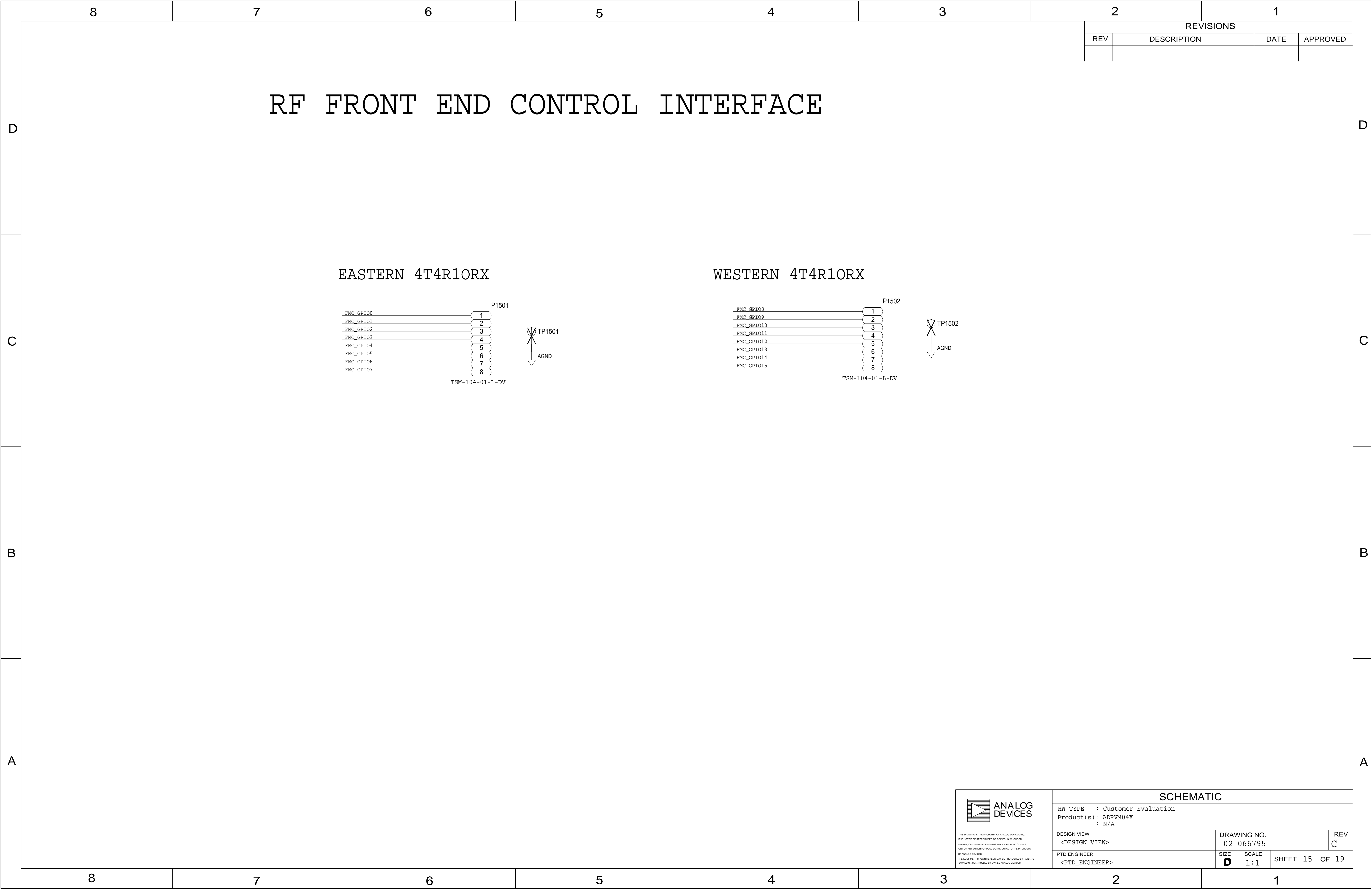
REVISIONS

REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): ADRV904X : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795	REV C	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 14 OF 19

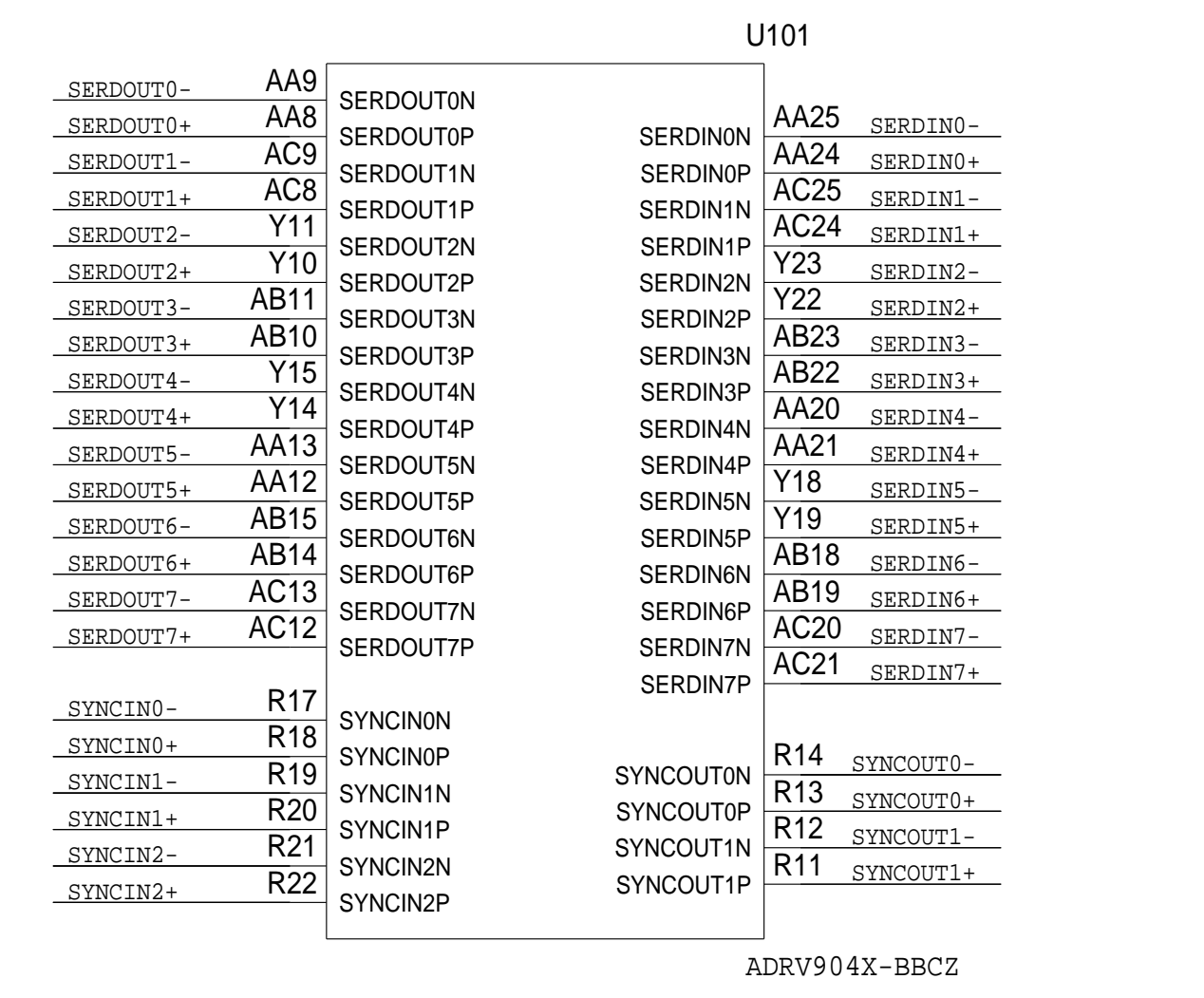
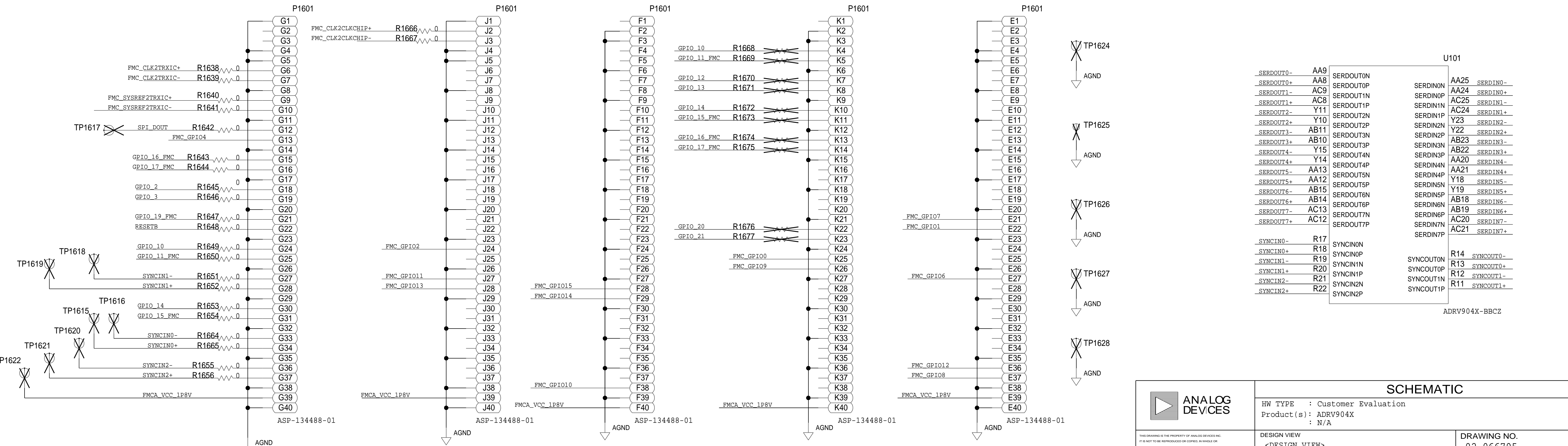
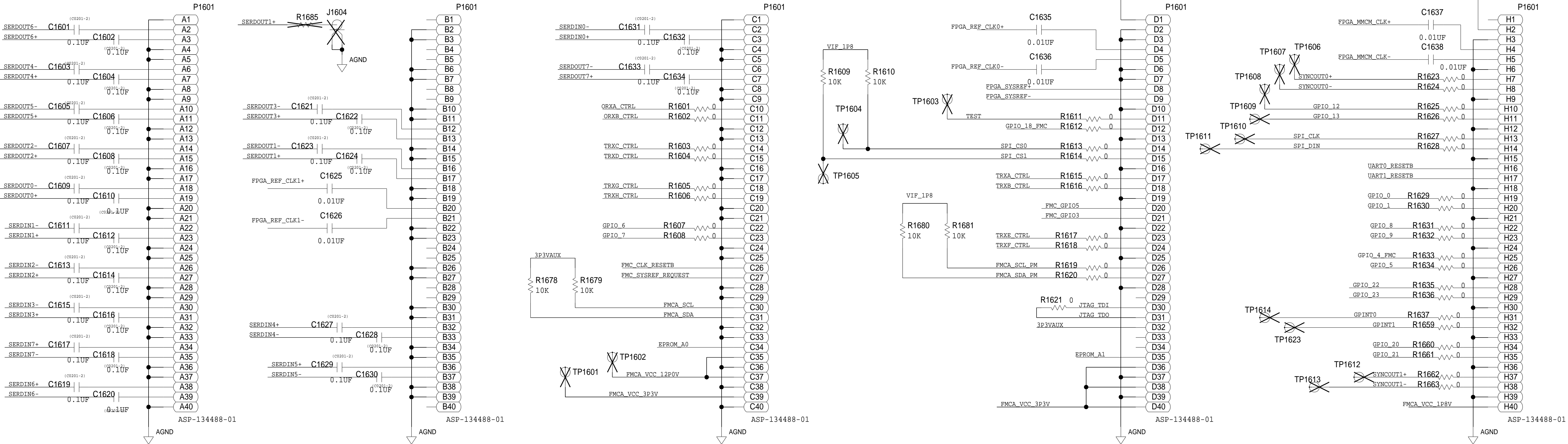
ADRV904X-BBC2





# FPGA CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADR904X : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066795	REV C	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 16 OF 19



